

	1	2	3	4	5	6	7	8																																																																																	
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A	HSF Property : Halogen-Free								A																																																																																
B	<div>LOUIS</div> <div>MV BUILD: VRAM x 4 pcs</div> <div>www.aitech1.ru</div> <div>Main Board 2011/09/01</div>								B																																																																																
C									C																																																																																
D									D																																																																																
E									E																																																																																
F	<div>SI BUILD: VRAM x 4pcs</div> <div>SAMSUNG-Main Board: 1310A2468601 (2D)</div> <div>HYNIX-Main Board: 1310A2468602 (2D)</div> <div>SAMSUNG-Main Board: 1310A2468603 (3D)</div> <div>HYNIX-Main Board: 1310A2468604 (3D)</div> <div>MB PCB Board's IEC PN: 6050A2468601</div>								F																																																																																
<table><tr><td></td><td></td><td></td></tr><tr><td></td><td></td><td></td></tr><tr><td></td><td></td><td></td></tr><tr><td></td><td></td><td></td></tr><tr><td></td><td></td><td></td></tr><tr><td></td><td></td><td></td></tr><tr><td></td><td></td><td></td></tr><tr><td></td><td></td><td></td></tr><tr><td></td><td></td><td></td></tr><tr><td>DATE</td><td>CHANGE NO.</td><td>REV</td></tr></table>																													DATE	CHANGE NO.	REV	<table><tr><td>DRAWER</td><td>EE</td><td>DATE</td><td>POWER</td><td>DATE</td><td rowspan="4">INVENTEC</td></tr><tr><td>DESIGN</td><td></td><td></td><td></td><td></td></tr><tr><td>CHECK</td><td></td><td></td><td></td><td></td></tr><tr><td>RESPONSIBLE</td><td></td><td></td><td></td><td></td></tr><tr><td colspan="5">SIZE = 3</td><td>VER :</td><td>SIZE A3</td><td>CODE CS</td><td>DOC. NUMBER 1310A24686</td><td>REV A02</td></tr><tr><td colspan="5">FILE NAME : XXXX-XXXXXX-XX</td><td colspan="5"></td></tr><tr><td colspan="5">P/N XXXXXXXXXXXX</td><td colspan="5">SHEET 1 OF 64</td></tr></table>						DRAWER	EE	DATE	POWER	DATE	INVENTEC	DESIGN					CHECK					RESPONSIBLE					SIZE = 3					VER :	SIZE A3	CODE CS	DOC. NUMBER 1310A24686	REV A02	FILE NAME : XXXX-XXXXXX-XX										P/N XXXXXXXXXXXX					SHEET 1 OF 64					
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P/N XXXXXXXXXXXX					SHEET 1 OF 64																																																																																				
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52. BLANK
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60. VRAM-1
61. VRAM-2
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63. BLANK
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- Daughter Board**
65. BLANK
66. AUDIO BOARD-1 & USB CONN
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68. AUDIO BOARD-2 Codec
69. AUDIO BOARD-3 MIC Jack
70. AUDIO BOARD-4 Rear Speaker
71. AUDIO BOARD-5 Subwoofer
72. LDPS BOARD
73. Card Reader & Wireless Audio
74. POWER BOARD
75. 3D IR & ESD BOARD

SM BUS address / I2C

Cypress: 0xE0, 0x40

Audio amp: 0xC0

G-Sensor: 0x30, 0x32, 0x38, 0x3A

P-Sensor: 0x20, 0x21, 0x22, 0x23, 0xB0, 0xB1, 0xB2

DGPU Thermal: 0x82

Thermal IC: 0x98, 0x9A

Battery: 0x16

Charger IC: 0x12

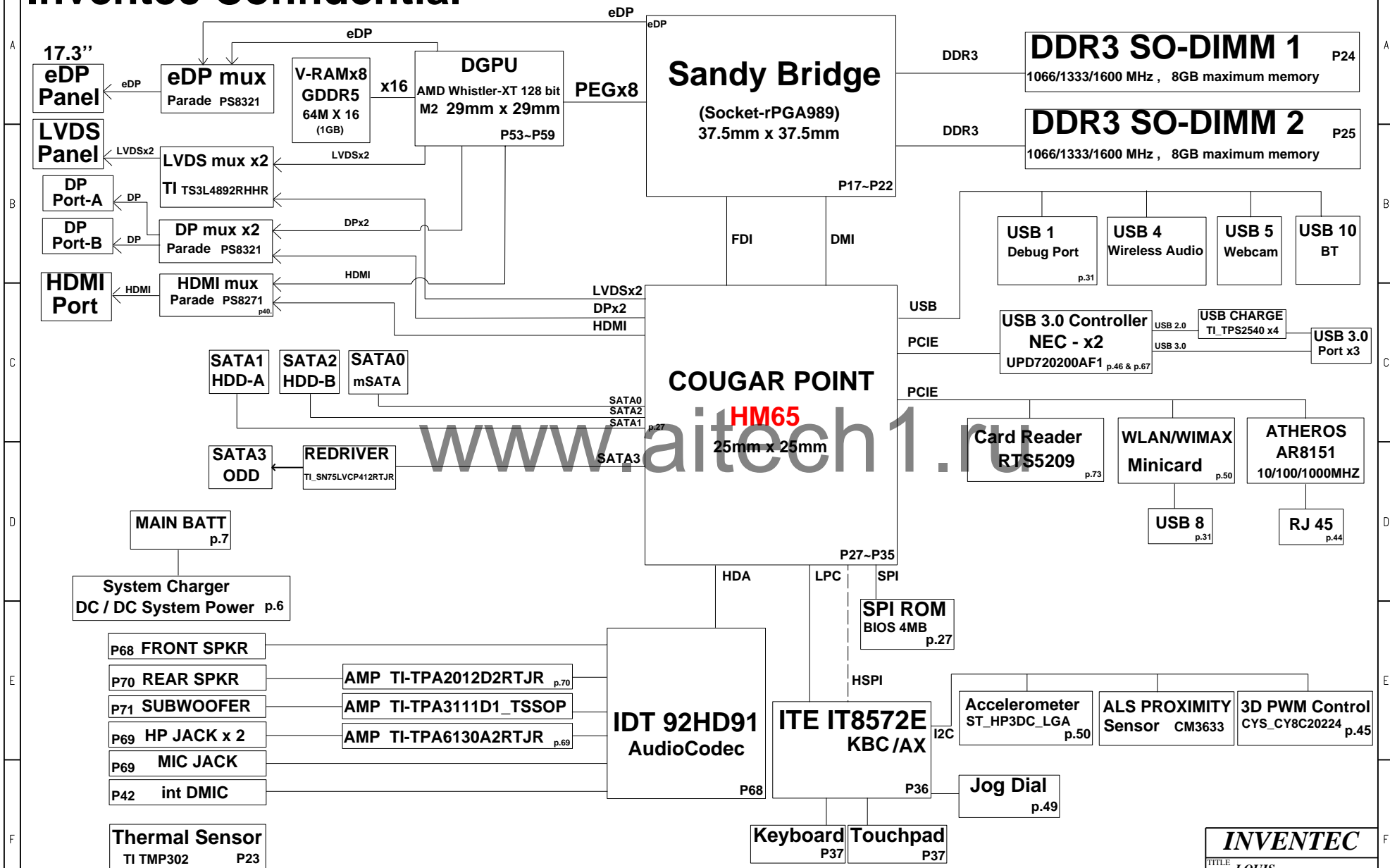
INVENTEC

TITLE
LOUIS
Index

SIZE A3	CODE CS	DOC. NUMBER <i>1310A24686</i>	REV <i>A02</i>
SHEET		2 OF 64	

CHANGE by *Wilson* 31-Aug-2011

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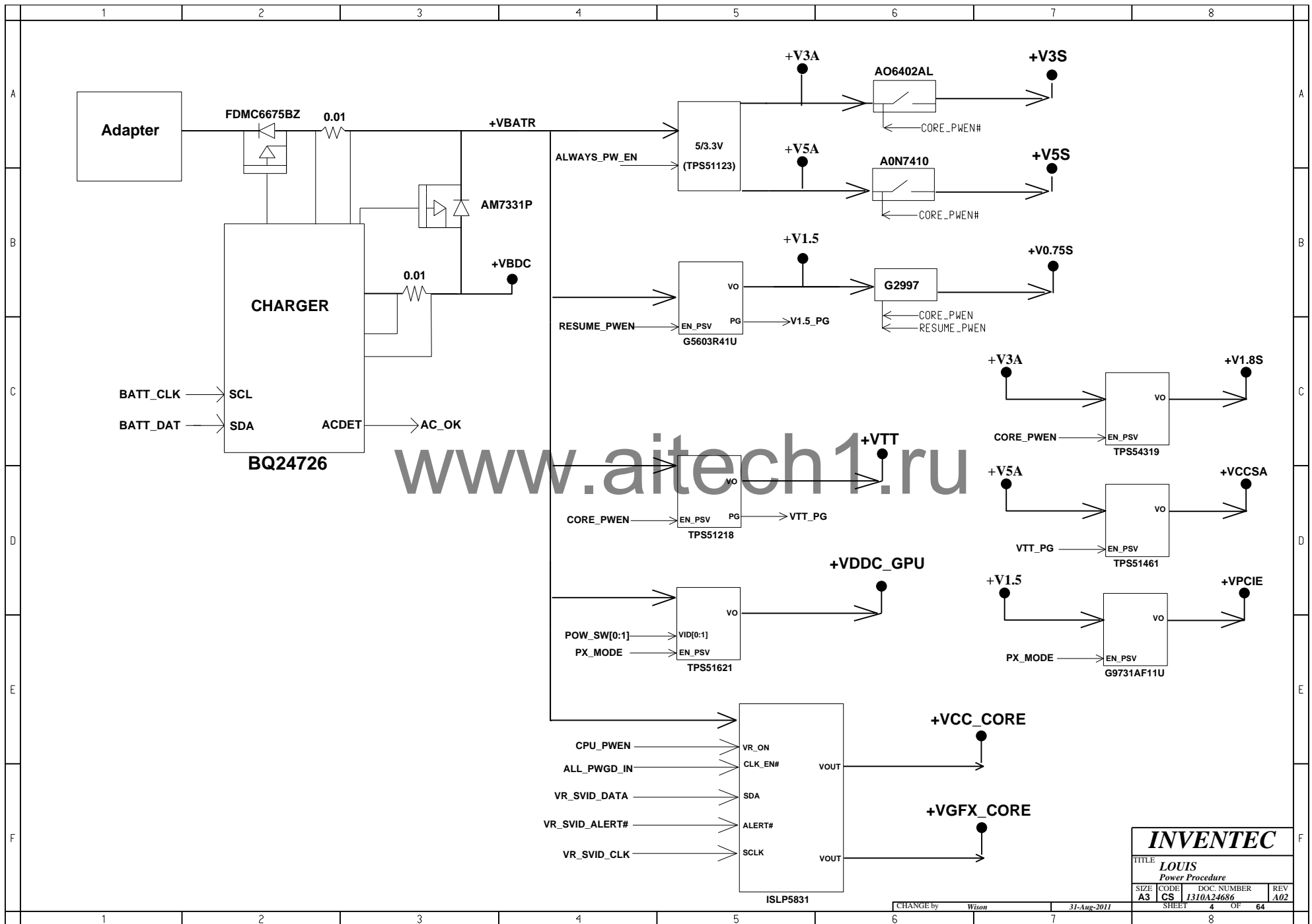


Board ID: page31

INVENTEC

TITLE: LOUIS Block Diagram			
SIZE: A3	CODE: CS	DOC. NUMBER: 1310A24686	REV: A02
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CHANGE by: Wilson 31-Aug-2011



INVENTEC

TITLE			
LOUIS			
Power Procedure			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	1310A24686	A02

CHANGE by: Wison 31-Aug-2011

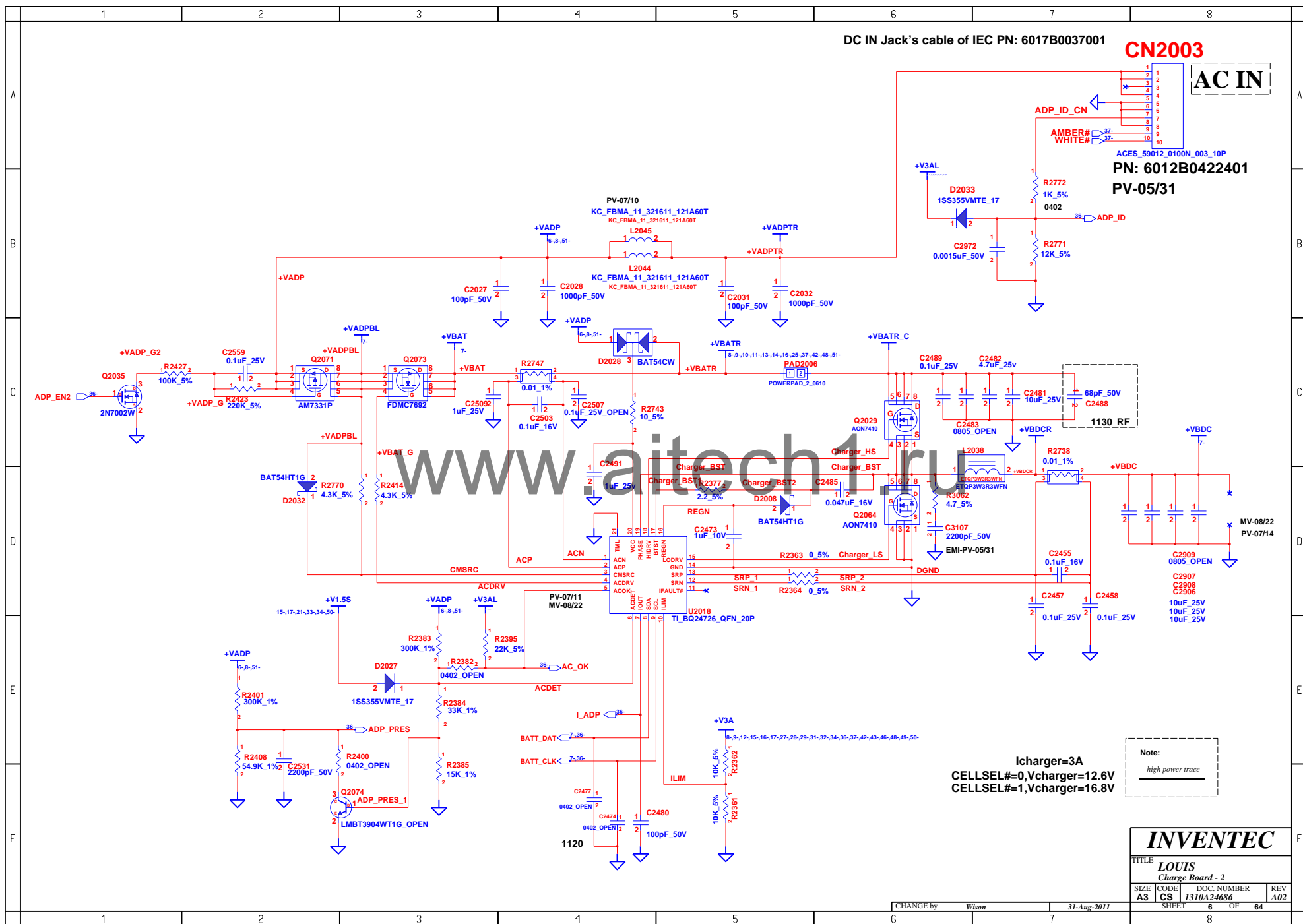
SHEET 4 OF 64

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B									B
C									C
D	<h1>www.aitech1.ru</h1>								D
E									E
F									F
	1	2	3	4	5	6	7	8	

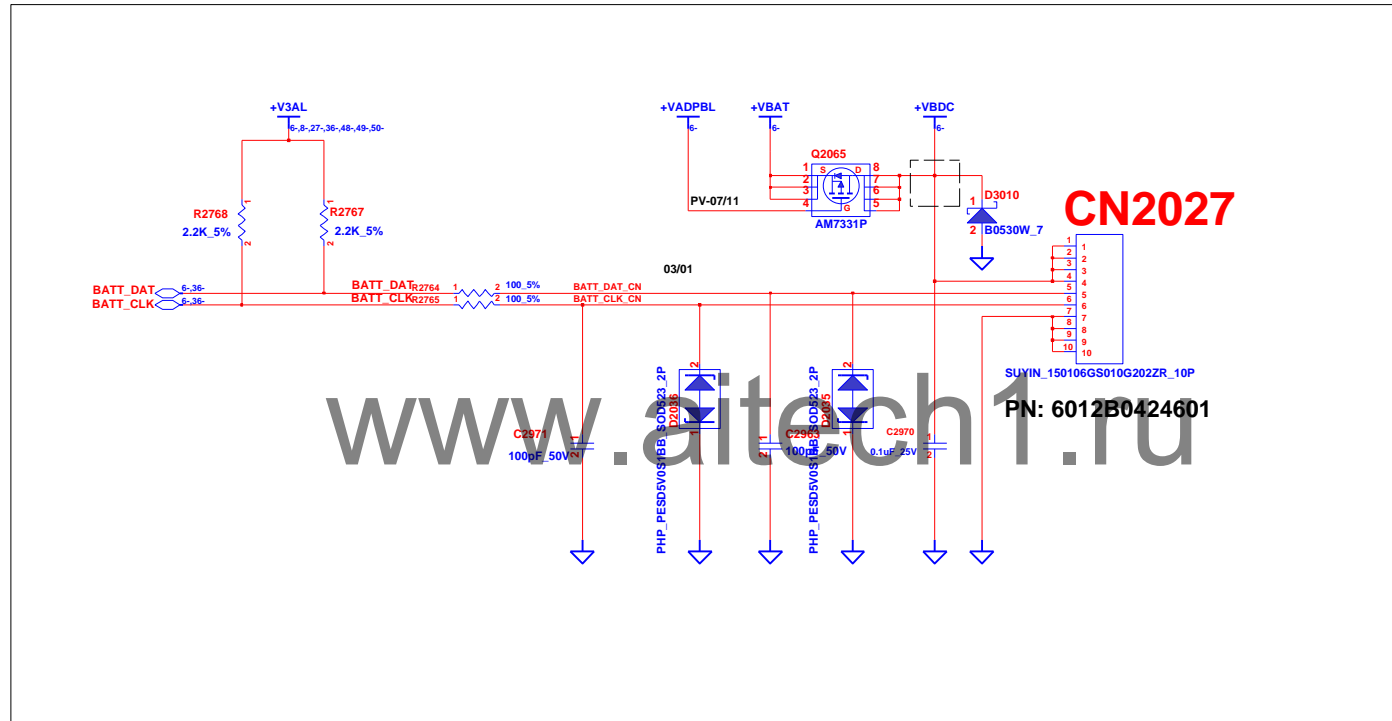
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LOUIS			
Power Procedure			
SIZE	CODE	DOC. NUMBER	REV
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CHANGE by *Wison* 31-Aug-2011



Battery Connector



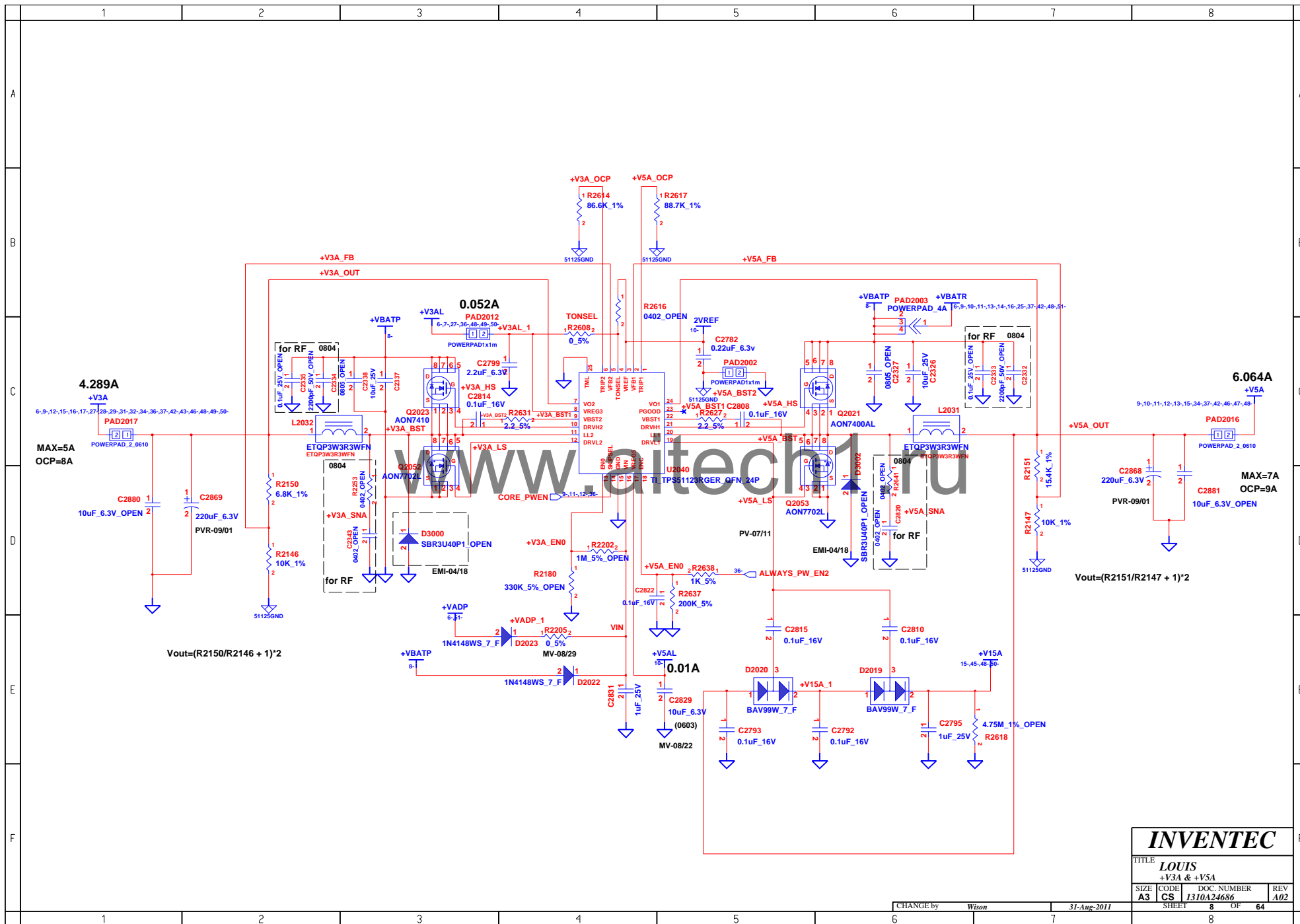
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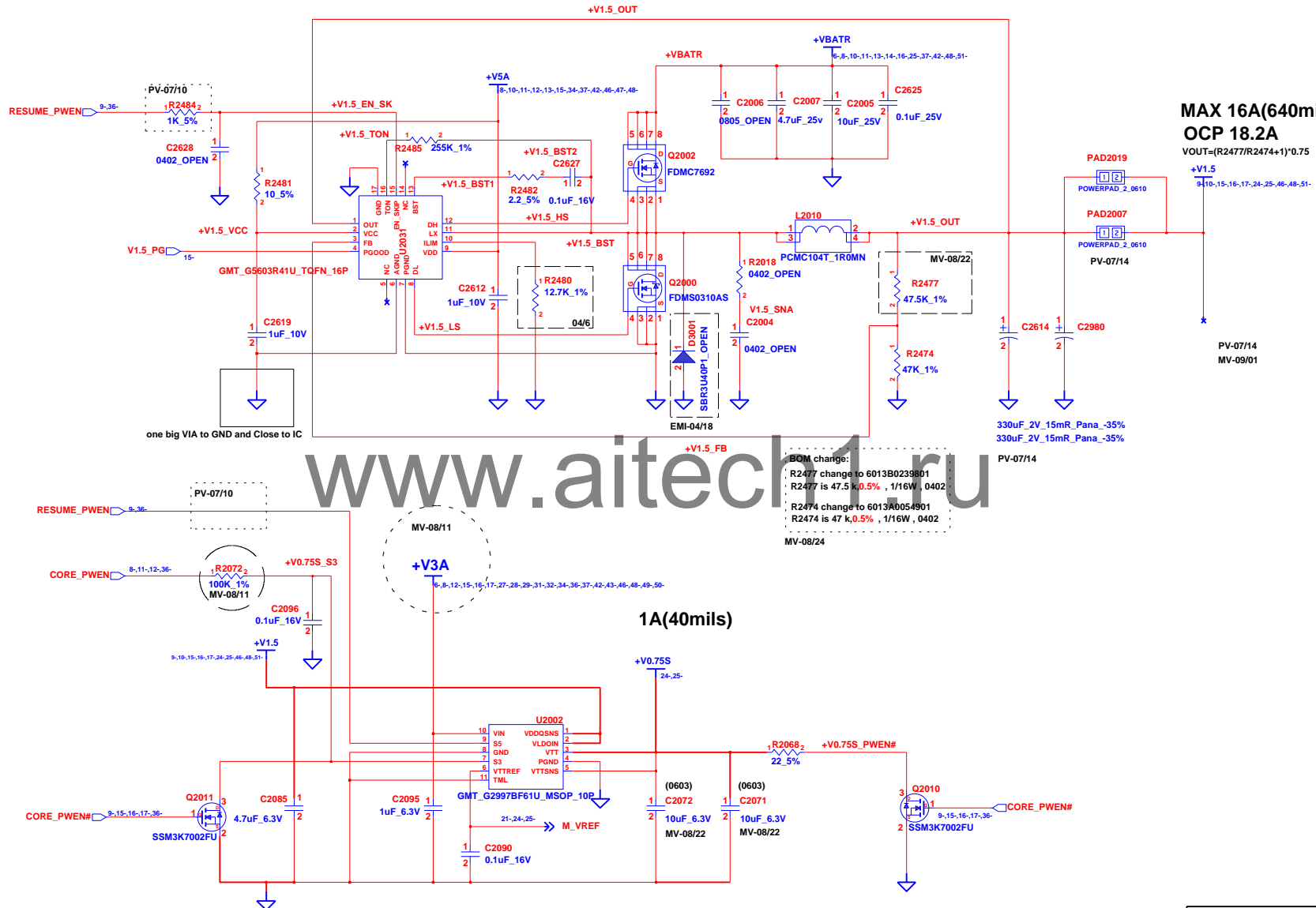
TITLE
LOUIS
Charge Board - 3

SIZE A3	CODE CS	DOC. NUMBER 1310A24686	REV A02
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CHANGE by **Wilson** 31-Aug-2011

SHEET **7** OF **64**





**MAX 16A(640mils)
OCP 18.2A**
 $V_{OUT} = (R2477/R2474 + 1) \cdot 0.75$

+V1.5
 9,10,15,16,17,24,25,46,48,51

PV-07/14
 MV-09/01

1A(40mils)

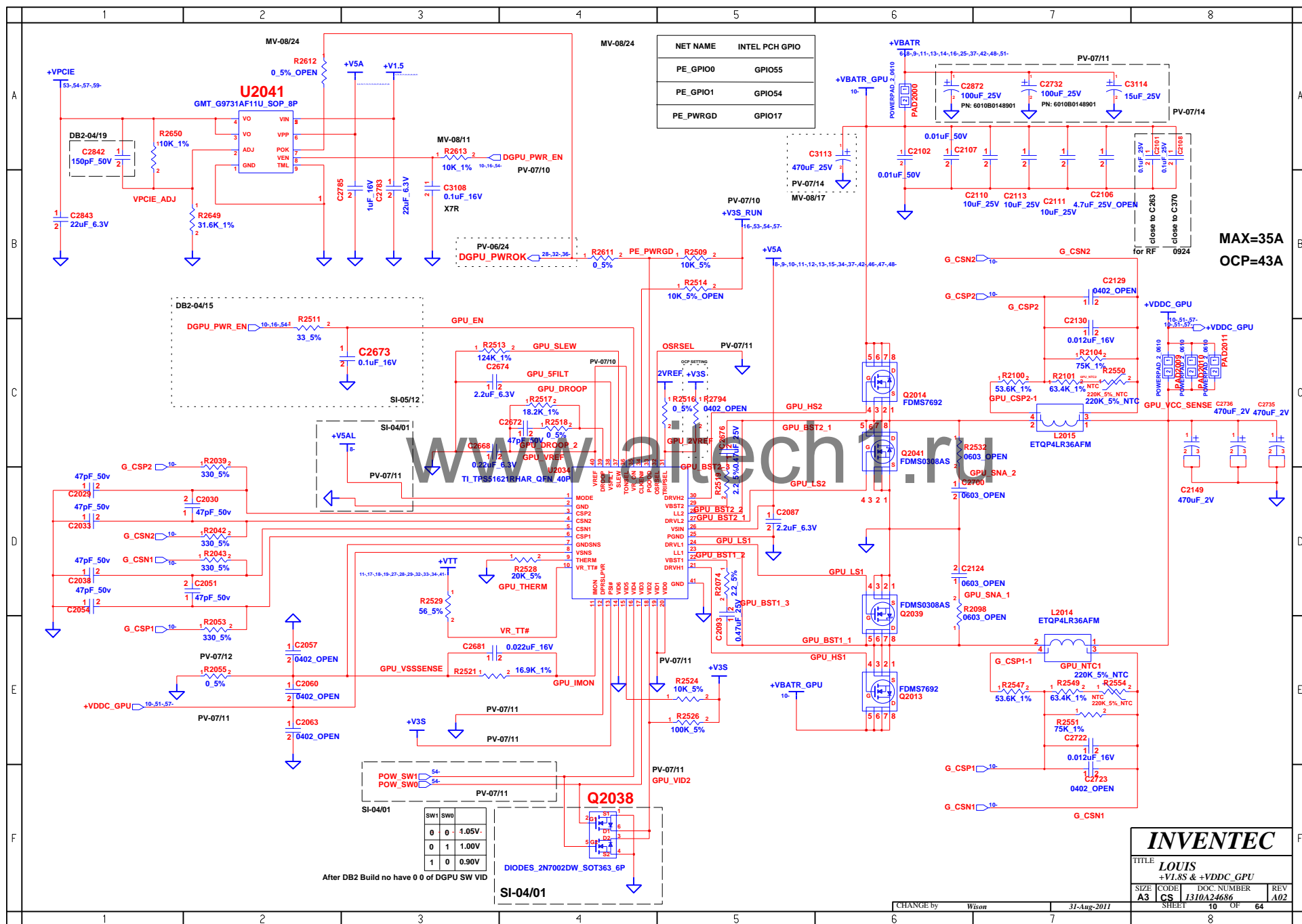
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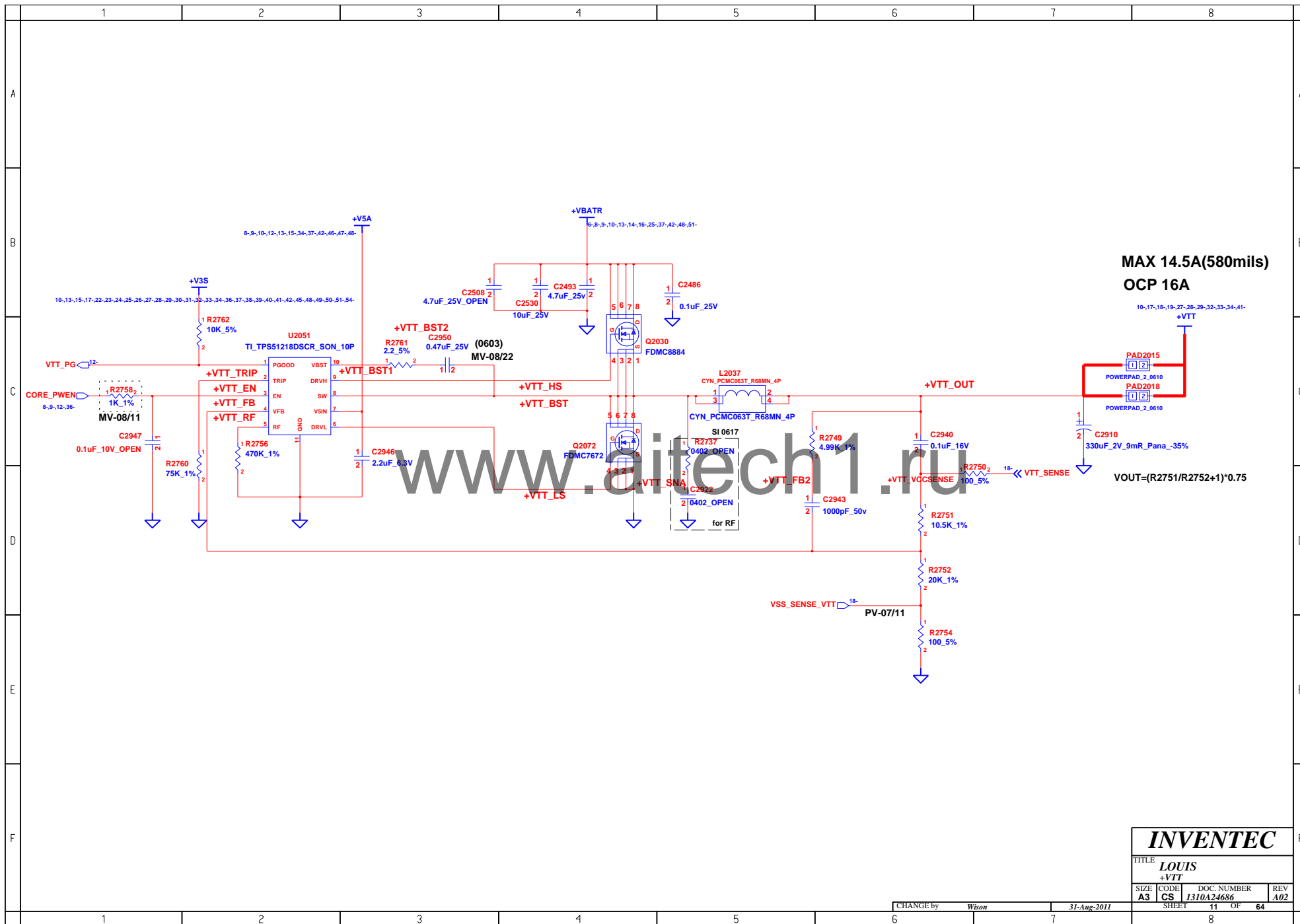
TITLE
LOUIS
 +V1.5 & +V0.75

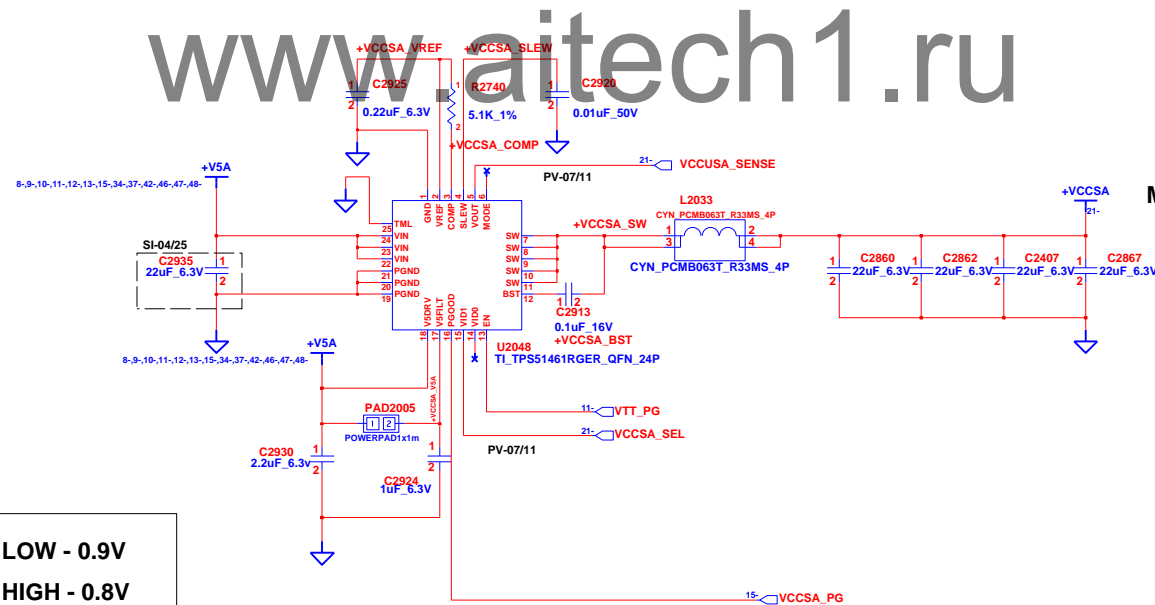
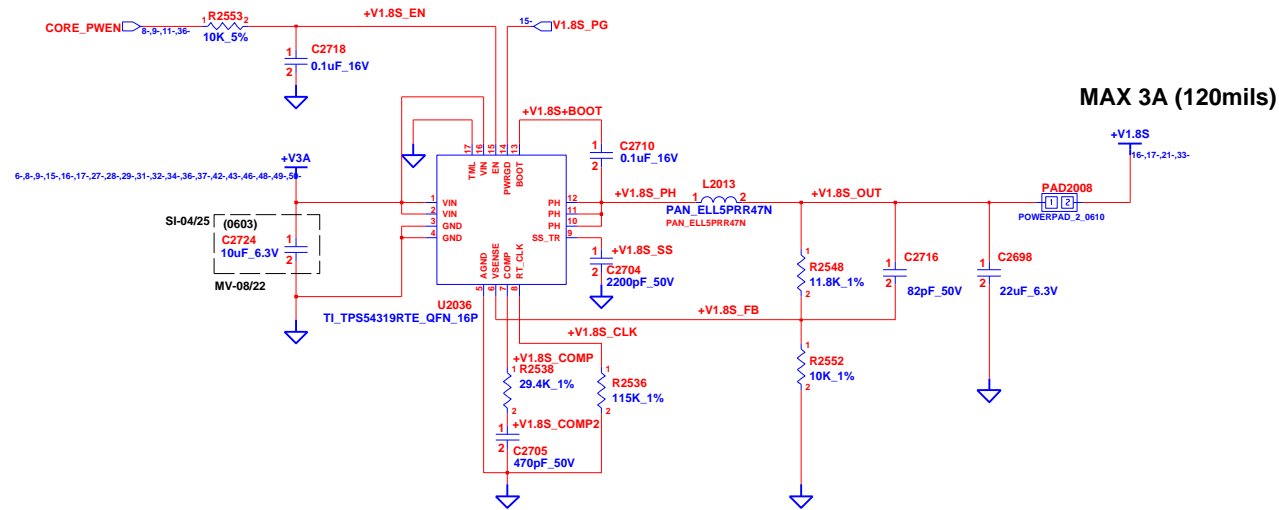
SIZE CODE DOC NUMBER REV
A3 CS 1310A24686 A02

CHANGE by **Wison** 31-Aug-2011

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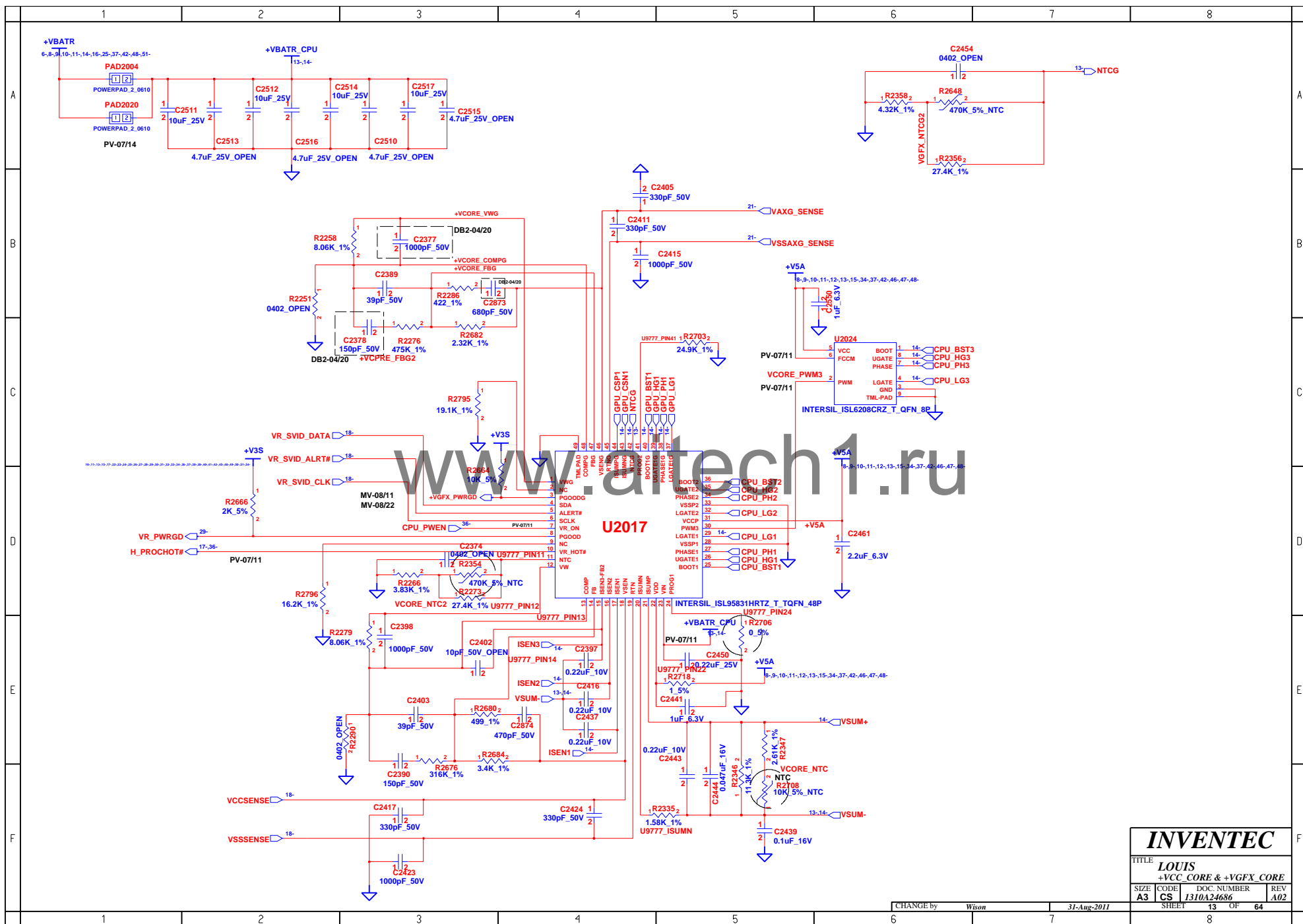


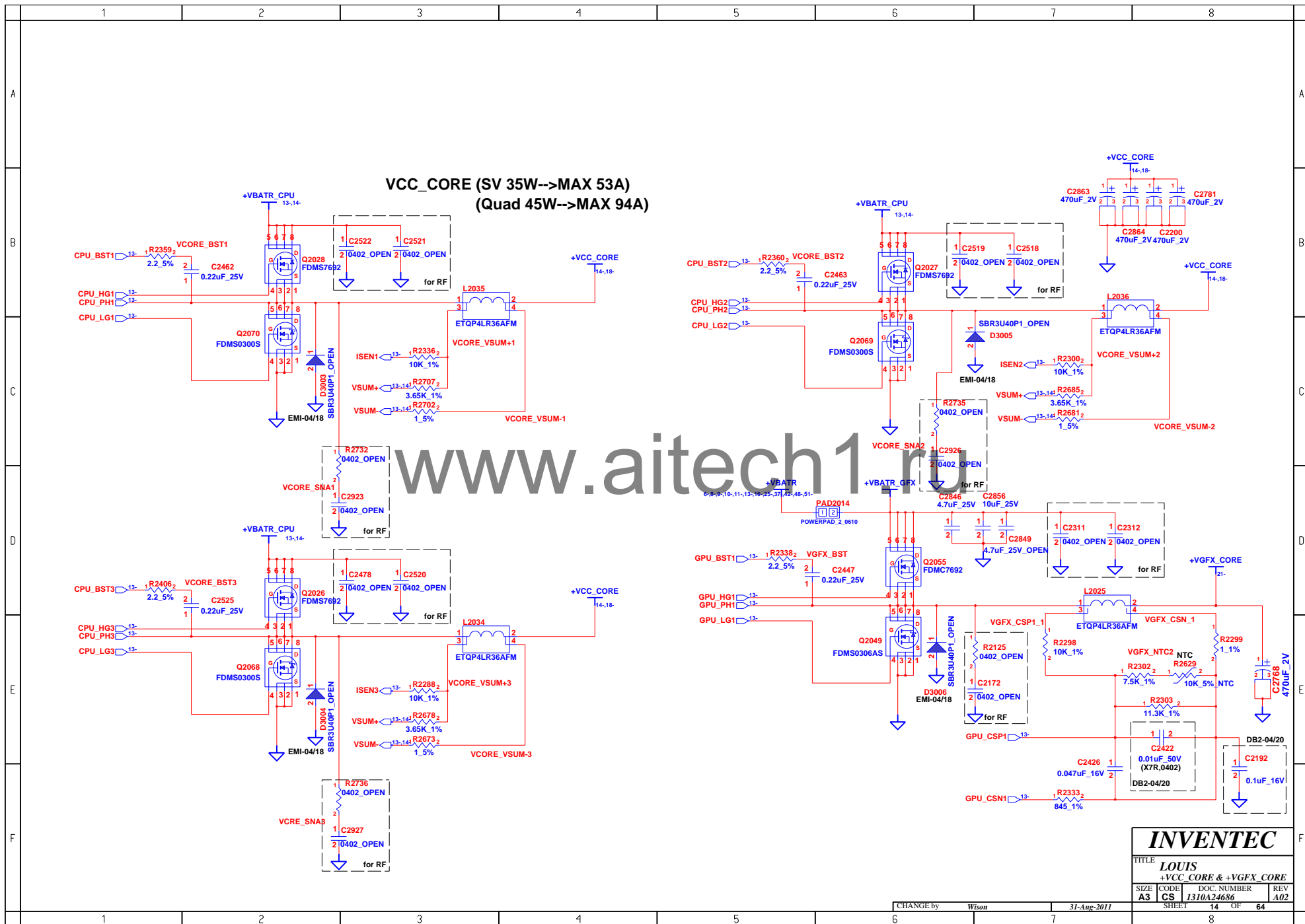
LOW - 0.9V
HIGH - 0.8V

INVENTEC

TITLE LOUIS +VCCSA			
SIZE A3	CODE CS	DOC. NUMBER 1310A24686	REV A02
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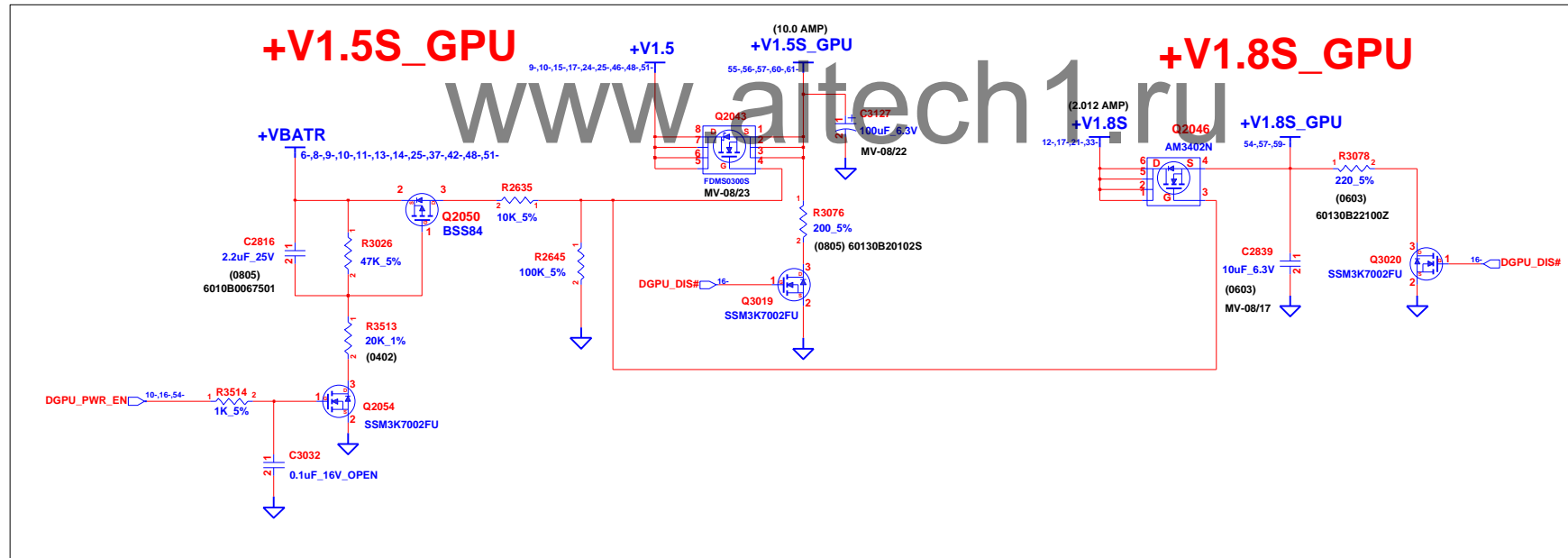
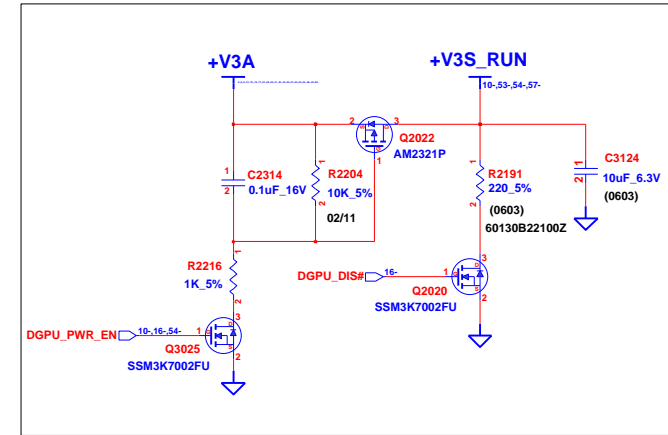
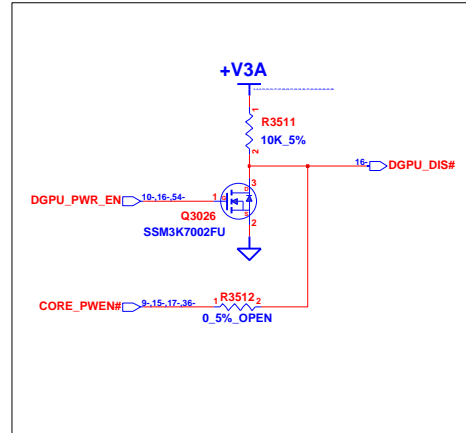
CHANGE by **Wilson** 31-Aug-2011





DGPU Power

MV-08/11



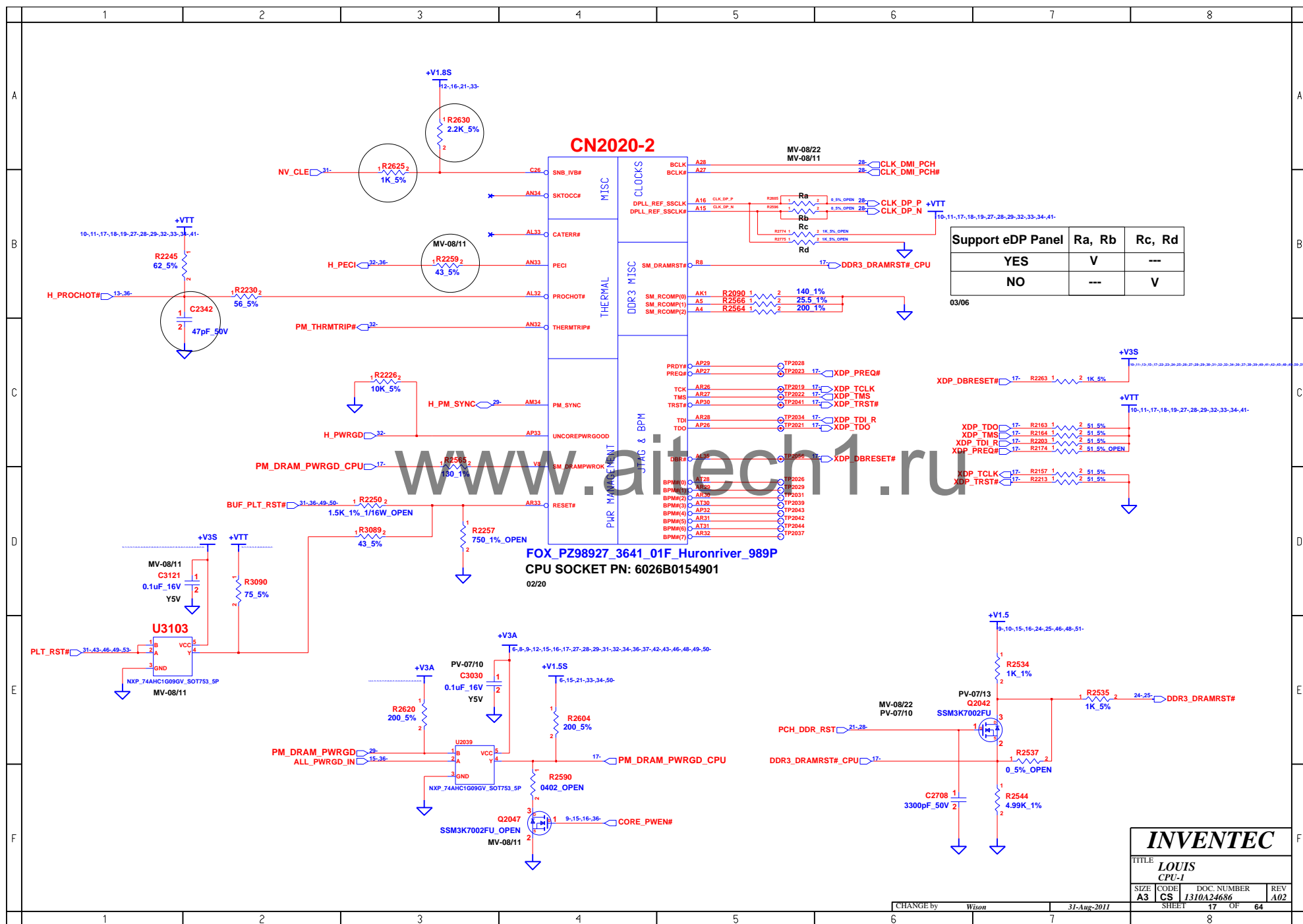
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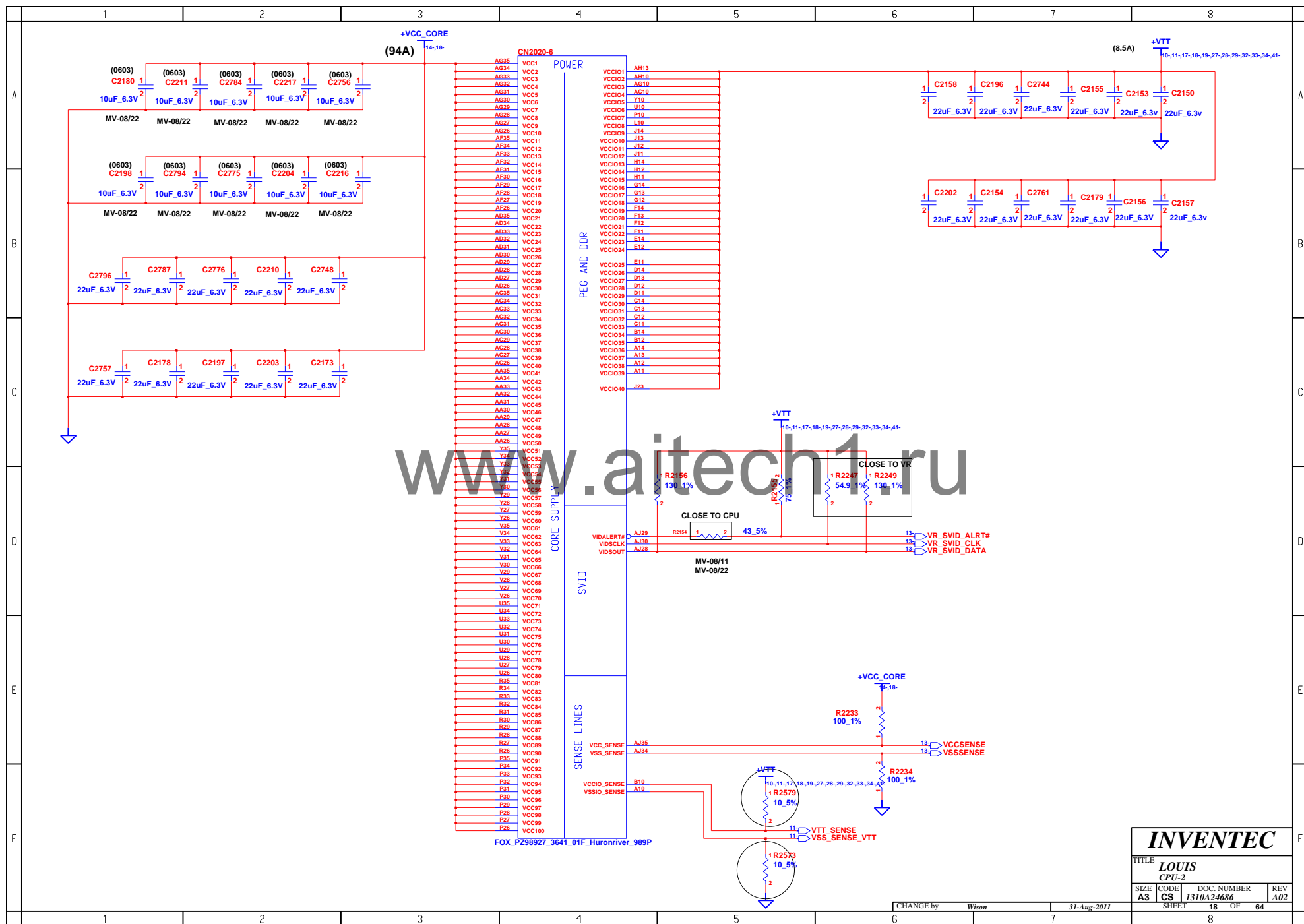
TITLE **LOUIS DGPU Power**

SIZE **A3** CODE **CS** DOC. NUMBER **1310A24686** REV **A02**

CHANGE by **Wilson** 31-Aug-2011

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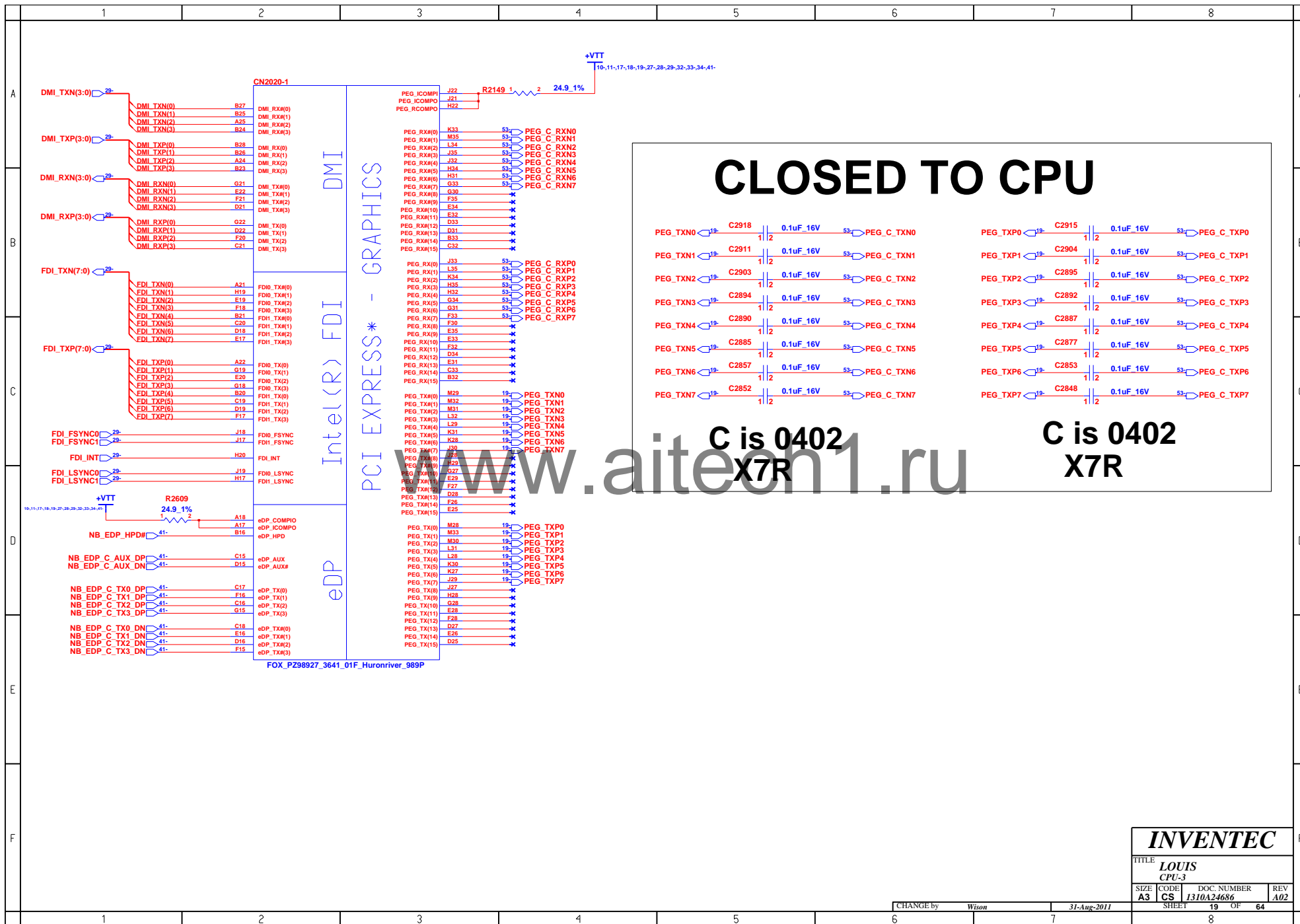
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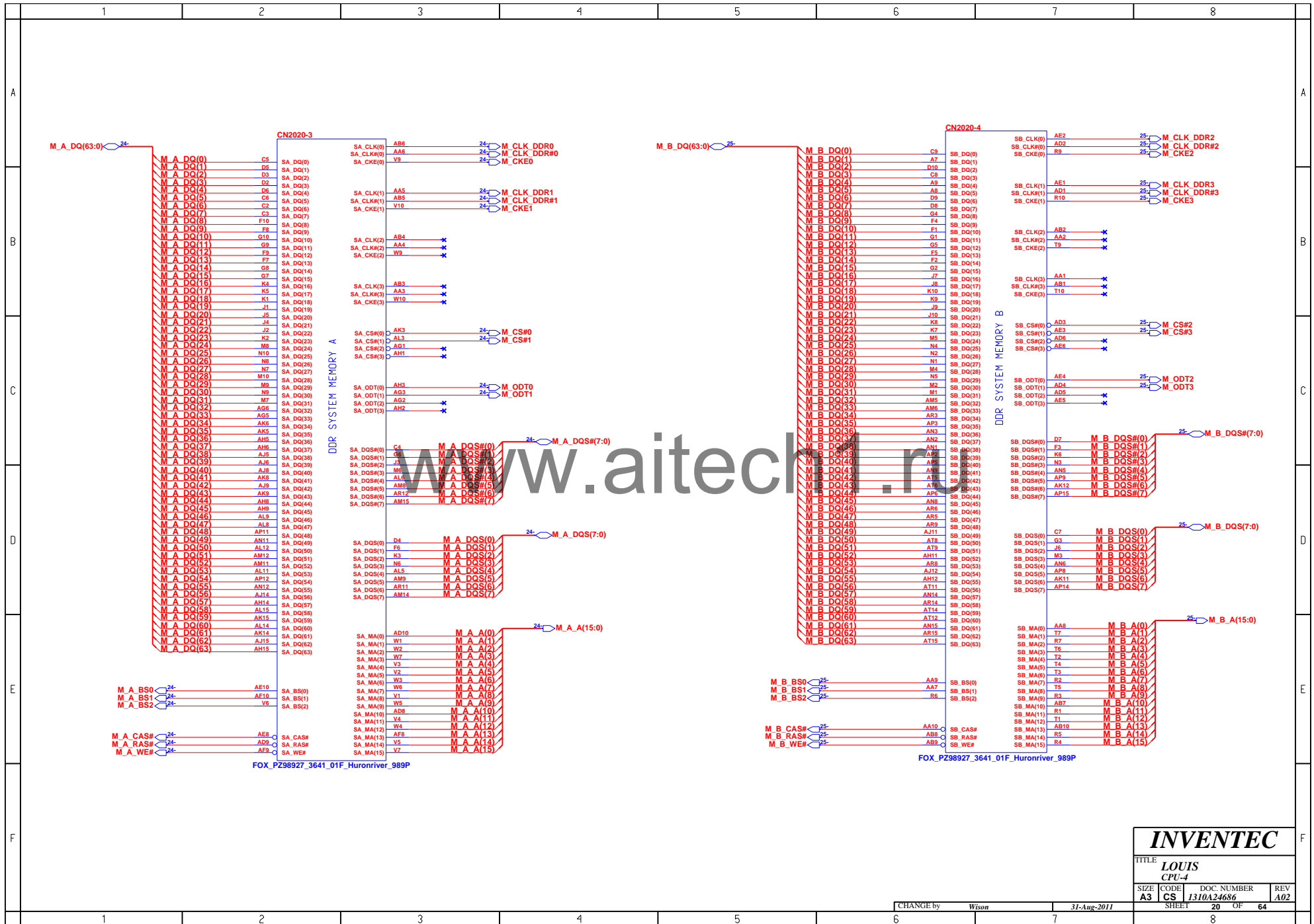
TITLE
LOUIS
CPU-2

SIZE	CODE	DOC. NUMBER	REV
A3	CS	1310A24686	A02

SHEET 18 OF 64

CHANGE by Wison 31-Aug-2011



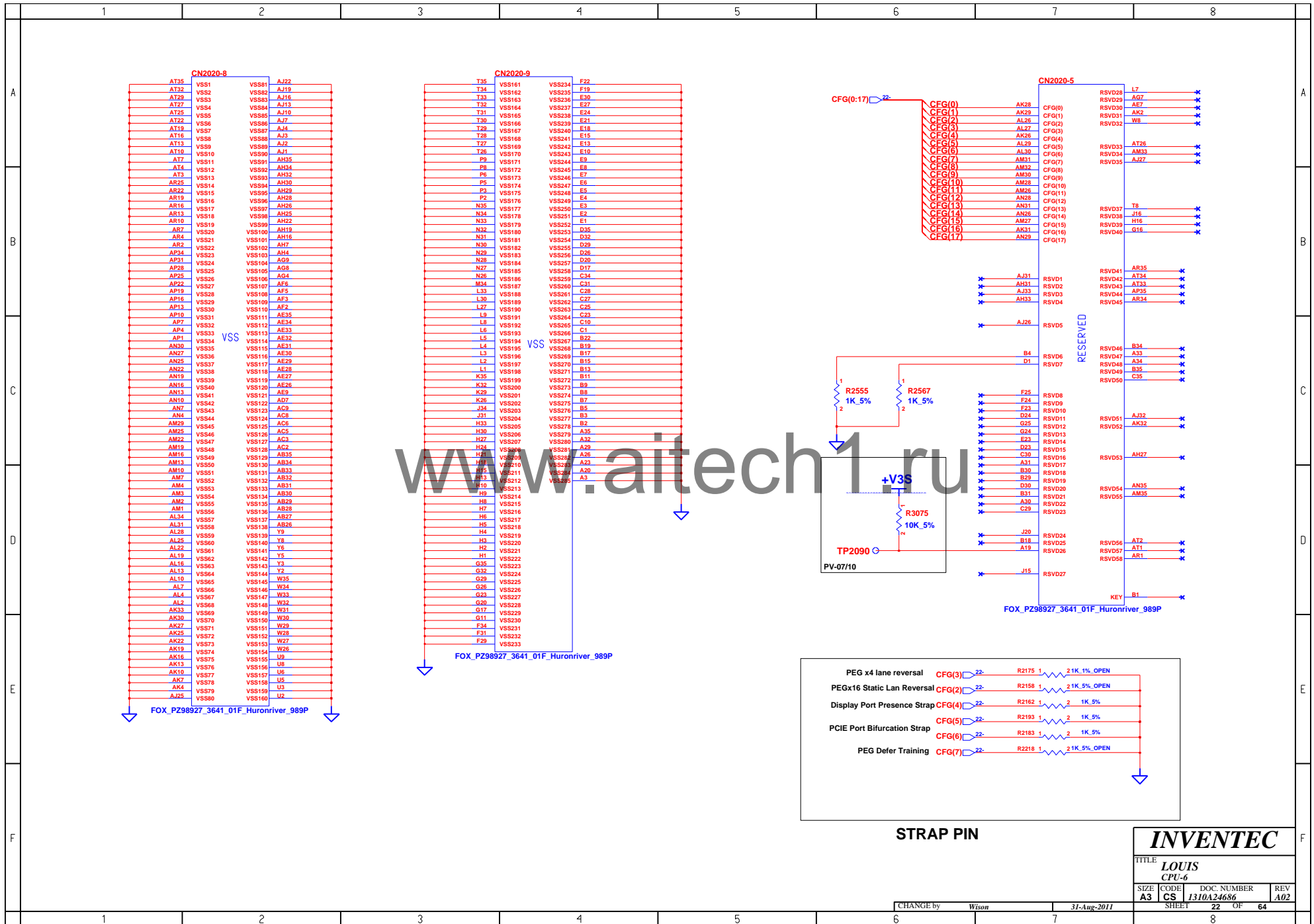


INVENTEC

TITLE			
LOUIS			
CPU-4			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	1310A24686	A02

CHANGE by: Wison 31-Aug-2011

SHEET 20 OF 64



STRAP PIN

INVENTEC

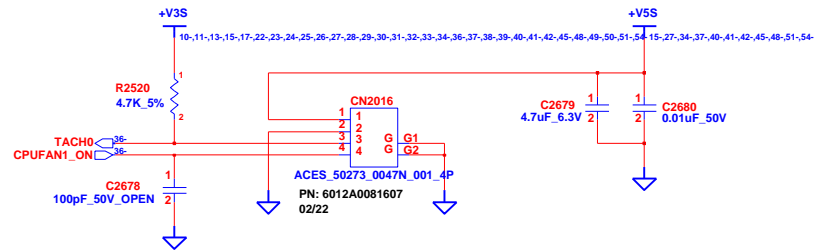
TITLE
LOUIS
CPU-6

SIZE	CODE	DOC. NUMBER	REV
A3	CS	1310A24686	A02

CHANGE by: Wilson 31-Aug-2011

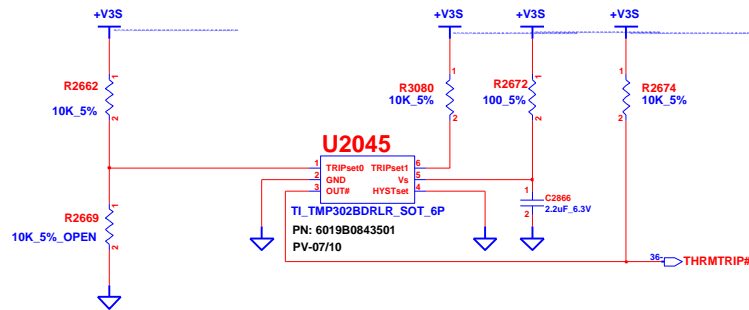
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FAN CONN



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PV-07/11

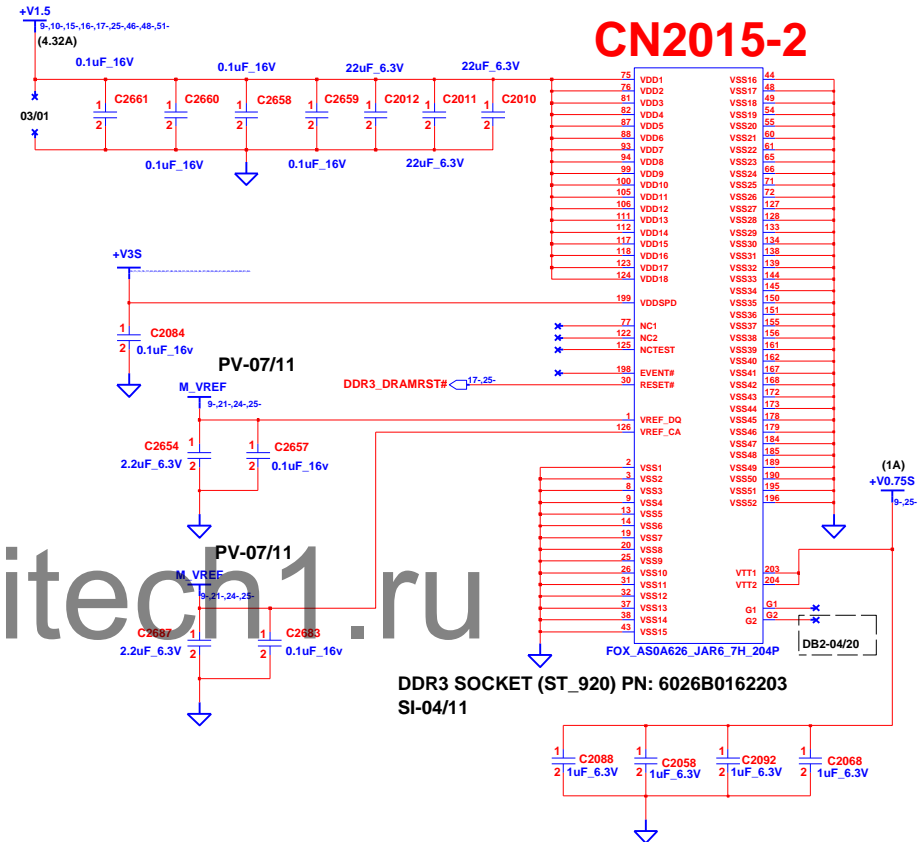


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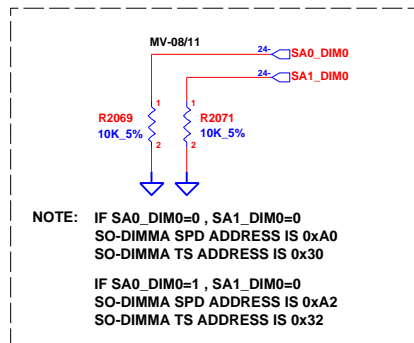
TITLE			
LOUIS			
FAN			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	1310A24686	A02
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CHANGE by *Wilson* 31-Aug-2011

CN2015-2



DDR3 SOCKET (ST_920) PN: 6026B0162203
SI-04/11

**INVENTEC**

TITLE	<i>LOUIS SO-DIMMI</i>
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SIZE A3	CODE CS	DOC. NUMBER 1310A24686	REV A02
SHEET 24 OF 64			

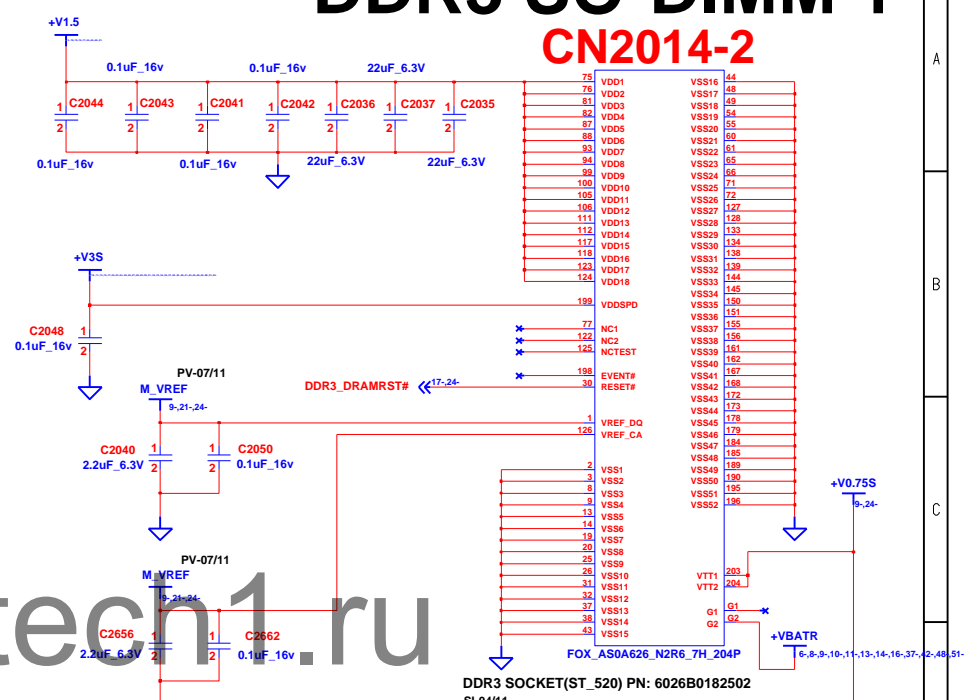
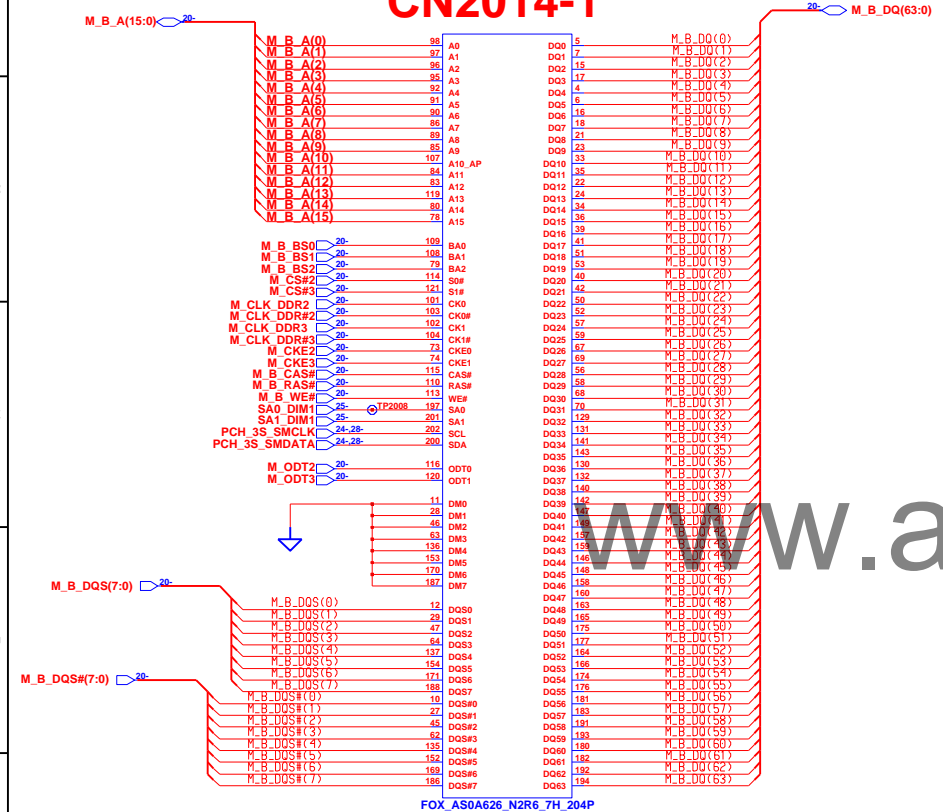
CHANGE by	<i>Wison</i>	<i>31-Aug-2011</i>
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24	OF	64
<hr/>		
8		

DDR3 SO-DIMM 1

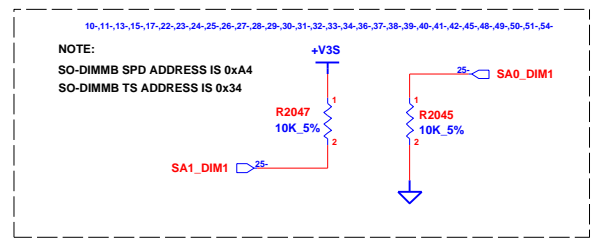
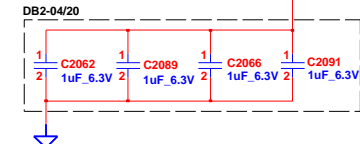
CN2014-2

CN2014-1



DDR3 SOCKET(ST_520) PN: 6026B0182502 SI-04/11

DDR3 SOCKET(ST_520) PN: 6026B0182502 SI-04/11



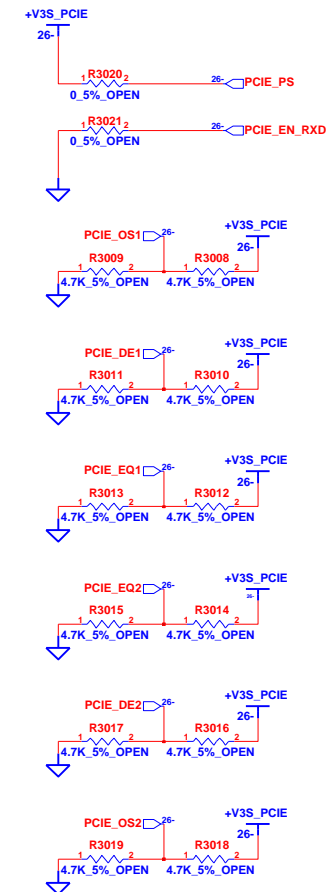
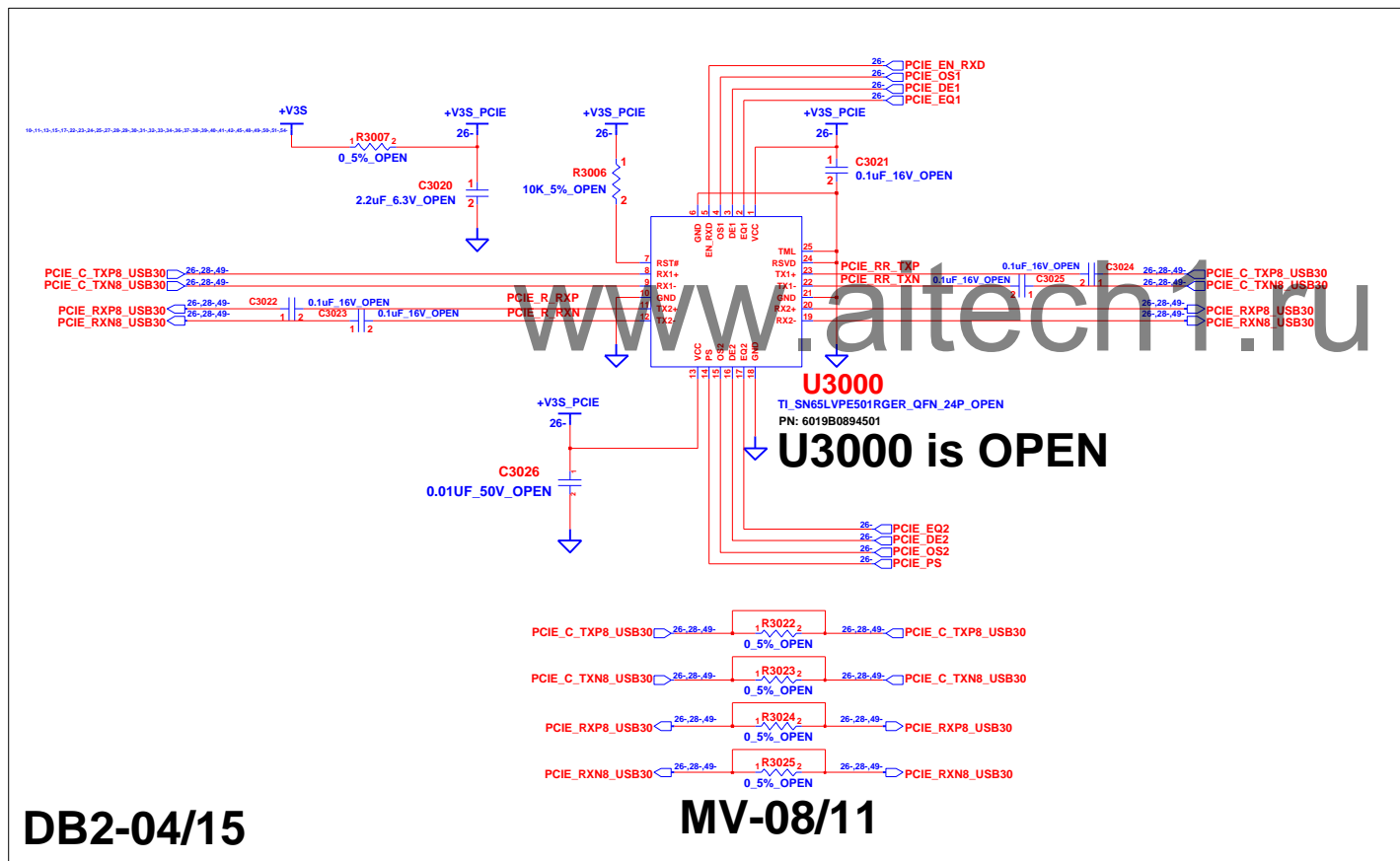
INVENTEC

TITLE LOUIS SO-DIMM2			
SIZE A3	CODE CS	DOC. NUMBER 1310A24686	REV A02
SHEET 25		OF 64	

CHANGE by Wison 31-Aug-2011

PCIE_REDRIIVER

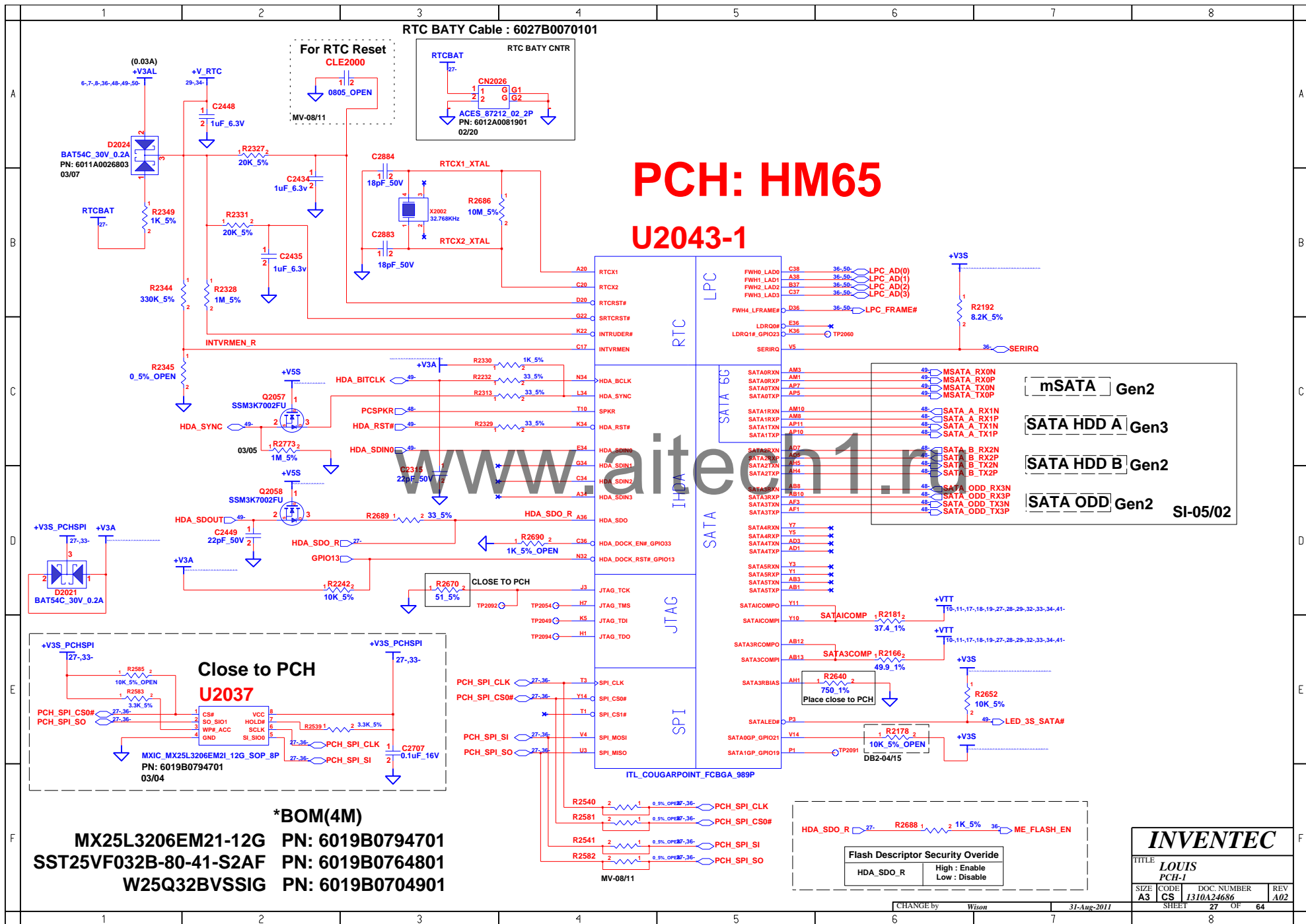
Co-Layout



INVENTEC

TITLE			
LOUIS			
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SIZE	CODE	DOC. NUMBER	REV
A3	CS	1310A24686	A02
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CHANGE by Wison 31-Aug-2011

[illegible]

**PCH: HM65
U2043-1**

BOM(4M)

MX25L3206EM21-12G	PN: 6019B0794701
SST25VF032B-80-41-S2AF	PN: 6019B0764801
W25Q32BVSSIG	PN: 6019B0704901

Flash Descriptor Security Override

HDA_SDO_R	High : Enable	Low : Disable

INVENTEC

TITLE LOUIS
PCH-I
SIZE A3 CODE CS DOC. NUMBER 1310A24686 REV A02
SHEET 27 OF 64

**PCH: HM65
U2043-1**

***BOM(4M)**

MX25L3206EM21-12G	PN: 6019B0794701
SST25VF032B-80-41-S2AF	PN: 6019B0764801
W25Q32BVSSIG	PN: 6019B0704901

Flash Descriptor Security Override

HDA_SDO_R	High : Enable	Low : Disable
[X]	[]	[]

INVENTEC

TITLE LOUIS
PCH-I
SIZE A3 CODE CS DOC. NUMBER 1310A24686 REV A02
SHEET 27 OF 64

**PCH: HM65
U2043-1**

***BOM(4M)**

MX25L3206EM21-12G	PN: 6019B0794701
SST25VF032B-80-41-S2AF	PN: 6019B0764801
W25Q32BVSSIG	PN: 6019B0704901

Flash Descriptor Security Override

HDA_SDO_R	High : Enable Low : Disable
[X]	[]

INVENTEC

TITLE LOUIS
PCH-1

SIZE CODE DOC. NUMBER REV
A3 CS 1310A24686 A02

SHEET 27 OF 64

**PCH: HM65
U2043-1**

***BOM(4M)**

MX25L3206EM21-12G	PN: 6019B0794701
SST25VF032B-80-41-S2AF	PN: 6019B0764801
W25Q32BVSSIG	PN: 6019B0704901

Flash Descriptor Security Override

HDA_SDO_R	High : Enable Low : Disable
[X]	[]

INVENTEC

TITLE LOUIS
PCH-1

SIZE CODE DOC. NUMBER REV
A3 CS 1310A24686 A02

SHEET 27 OF 64

**PCH: HM65
U2043-1**

BOM(4M)

MX25L3206EM21-12G	PN: 6019B0794701
SST25VF032B-80-41-S2AF	PN: 6019B0764801
W25Q32BVSSIG	PN: 6019B0704901

Flash Descriptor Security Override

HDA_SDO_R	High : Enable	Low : Disable

INVENTEC

TITLE LOUIS
PCH-I
SIZE A3 CODE CS DOC. NUMBER 1310A24686 REV A02
SHEET 27 OF 64

**PCH: HM65
U2043-1**

***BOM(4M)**

- MX25L3206EM21-12G PN: 6019B0794701
- SST25VF032B-80-41-S2AF PN: 6019B0764801
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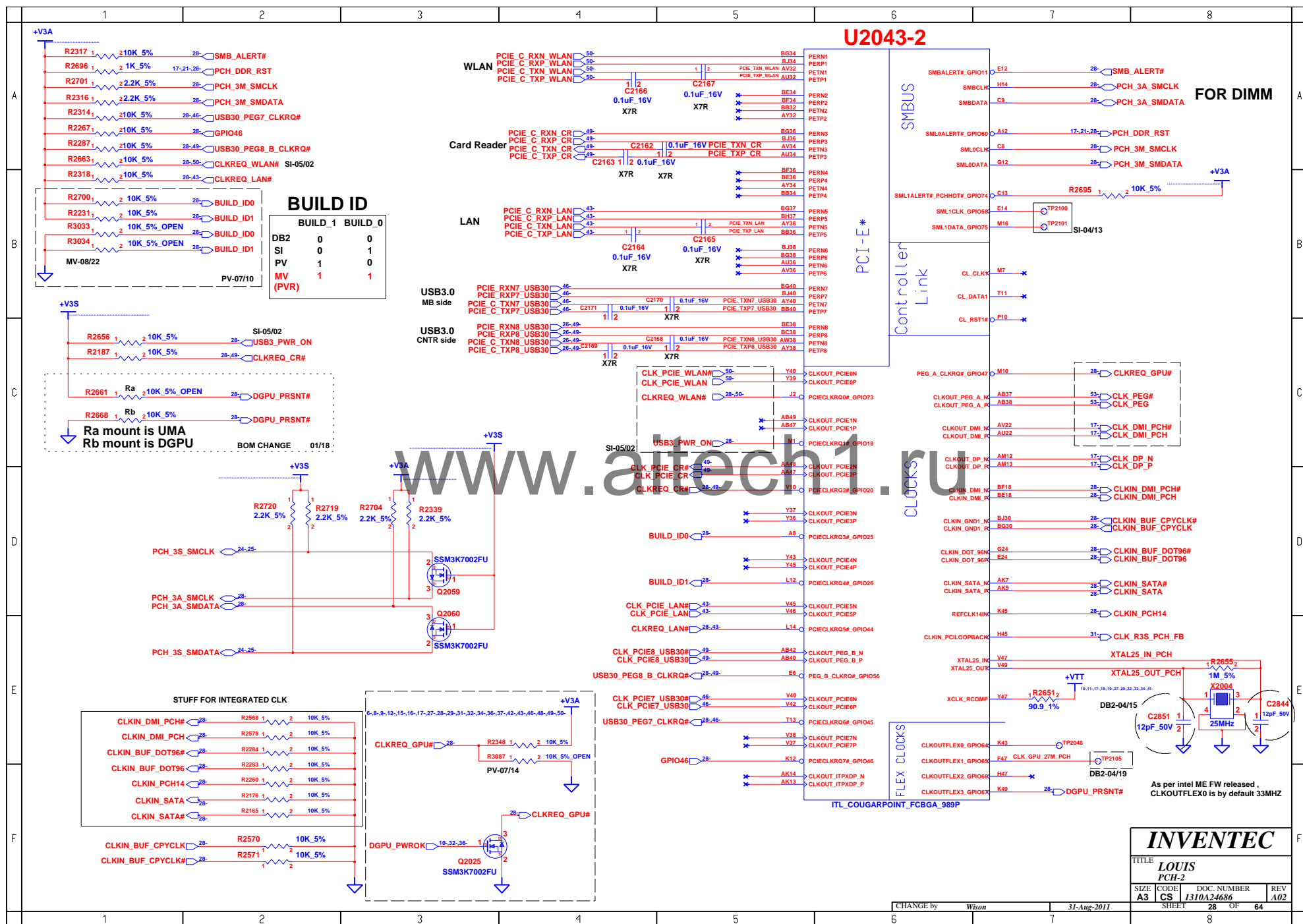
Flash Descriptor Security Override

HDA_SDO_R	High : Enable Low : Disable
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INVENTEC

TITLE LOUIS
PCH-I
SIZE A3 CODE CS DOC. NUMBER 1310A24686 REV A02
SHEET 27 OF 64

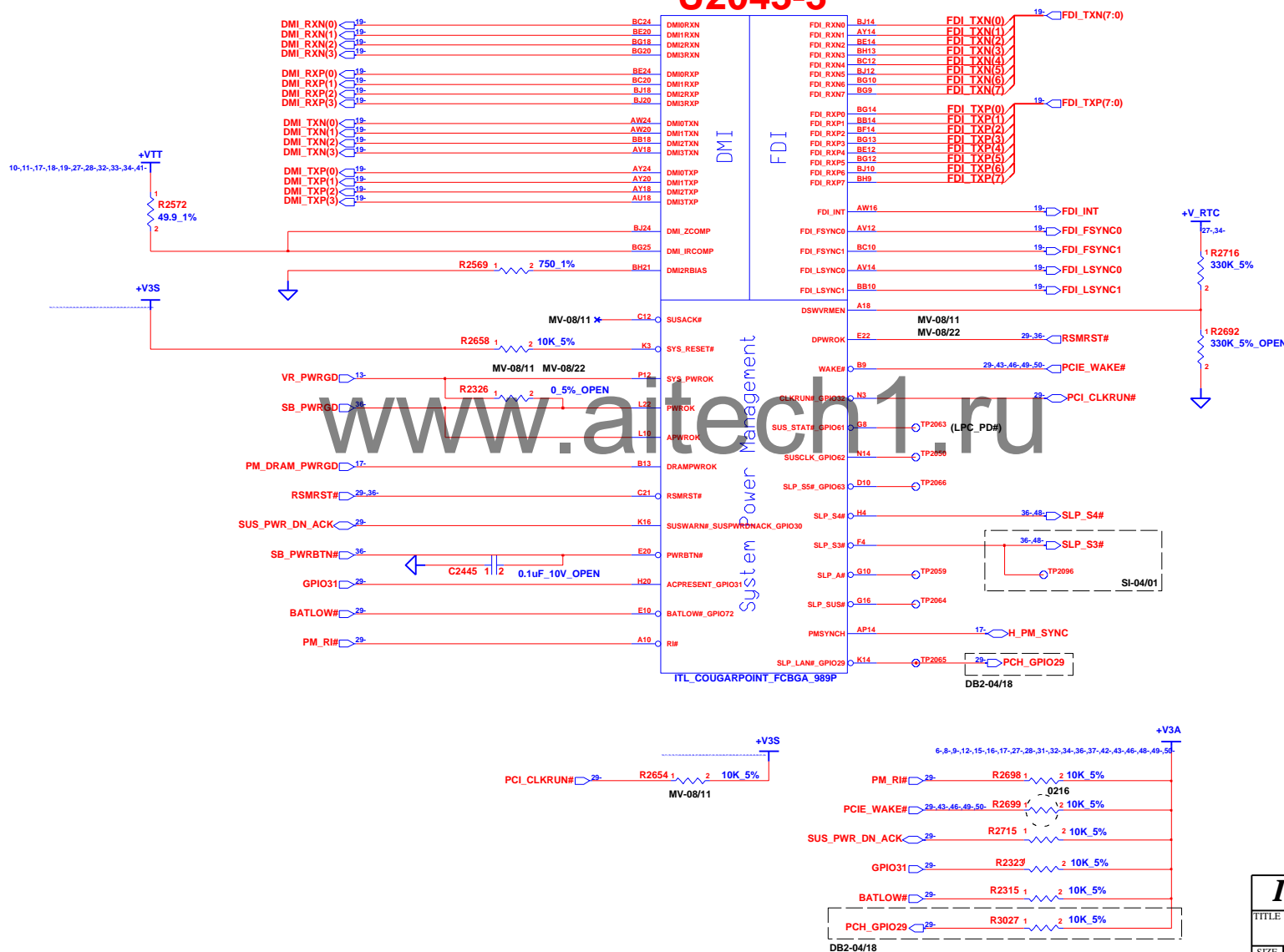
[illegible]



Note:

- 1.SLP_SUS and SUSACK# are NC if DSW is not supported
- 2.DPWROK should connect to RSMRST# if DSW not supported

U2043-3



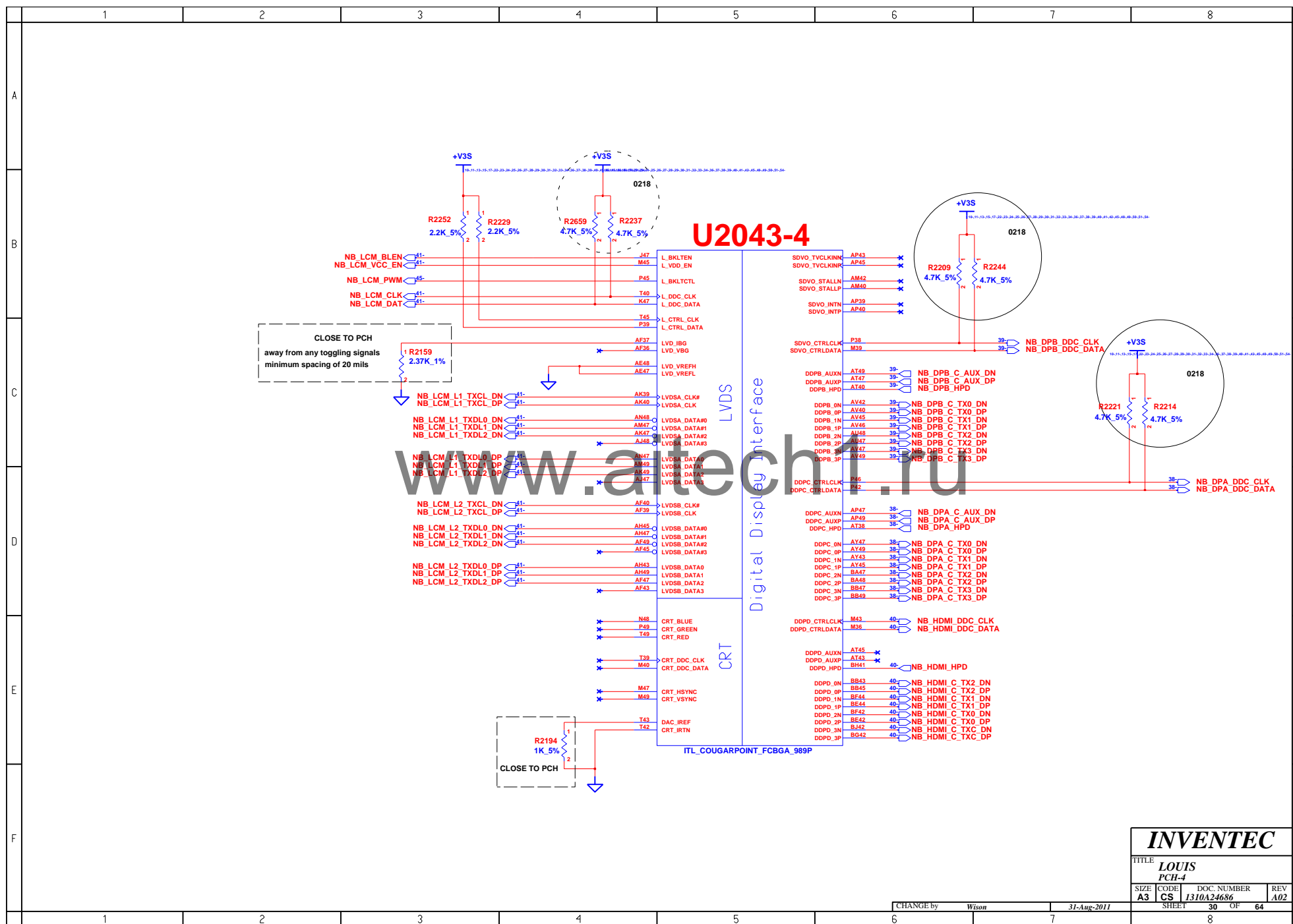
INVENTEC

TITLE
LOUIS
PCH-3

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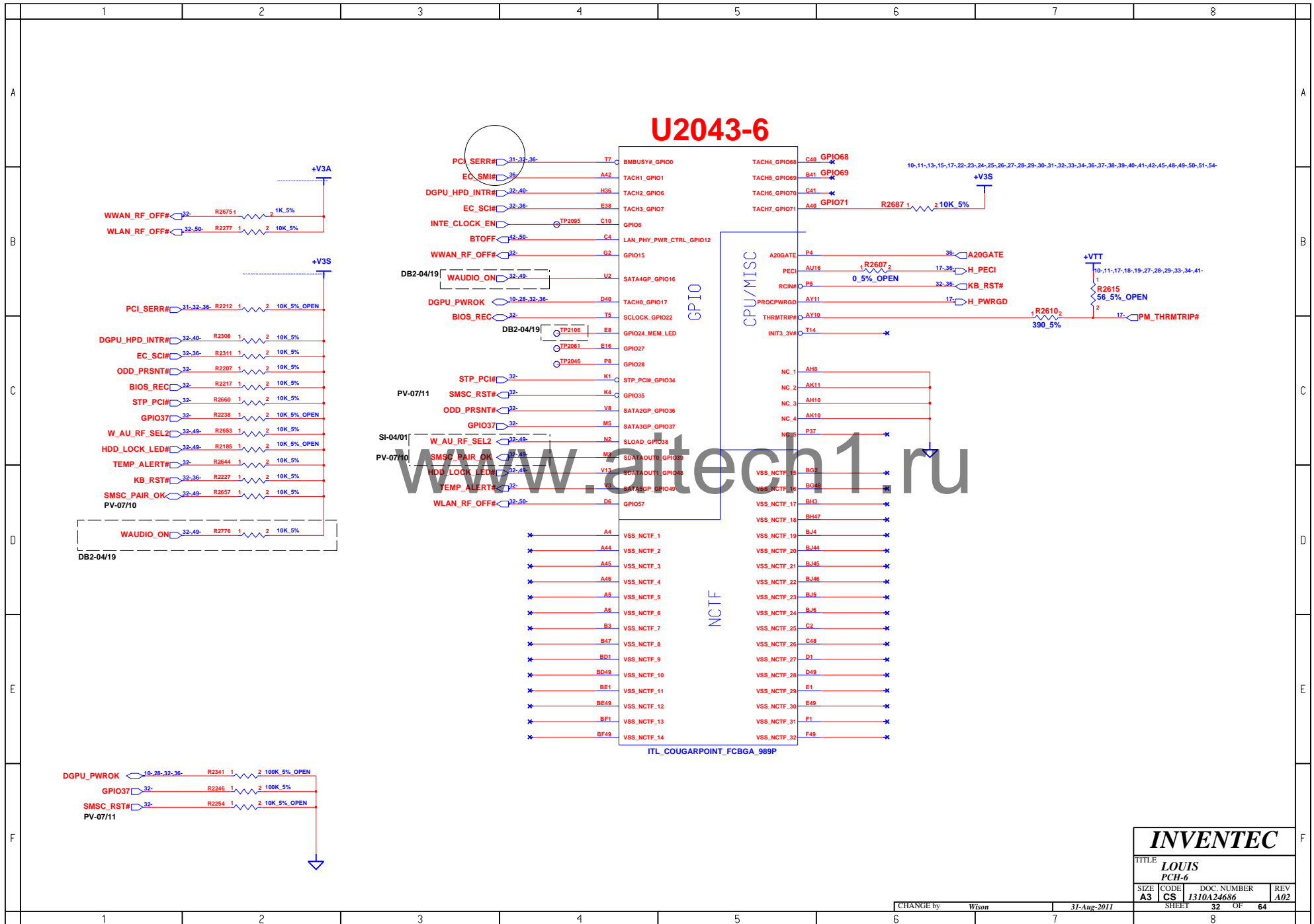
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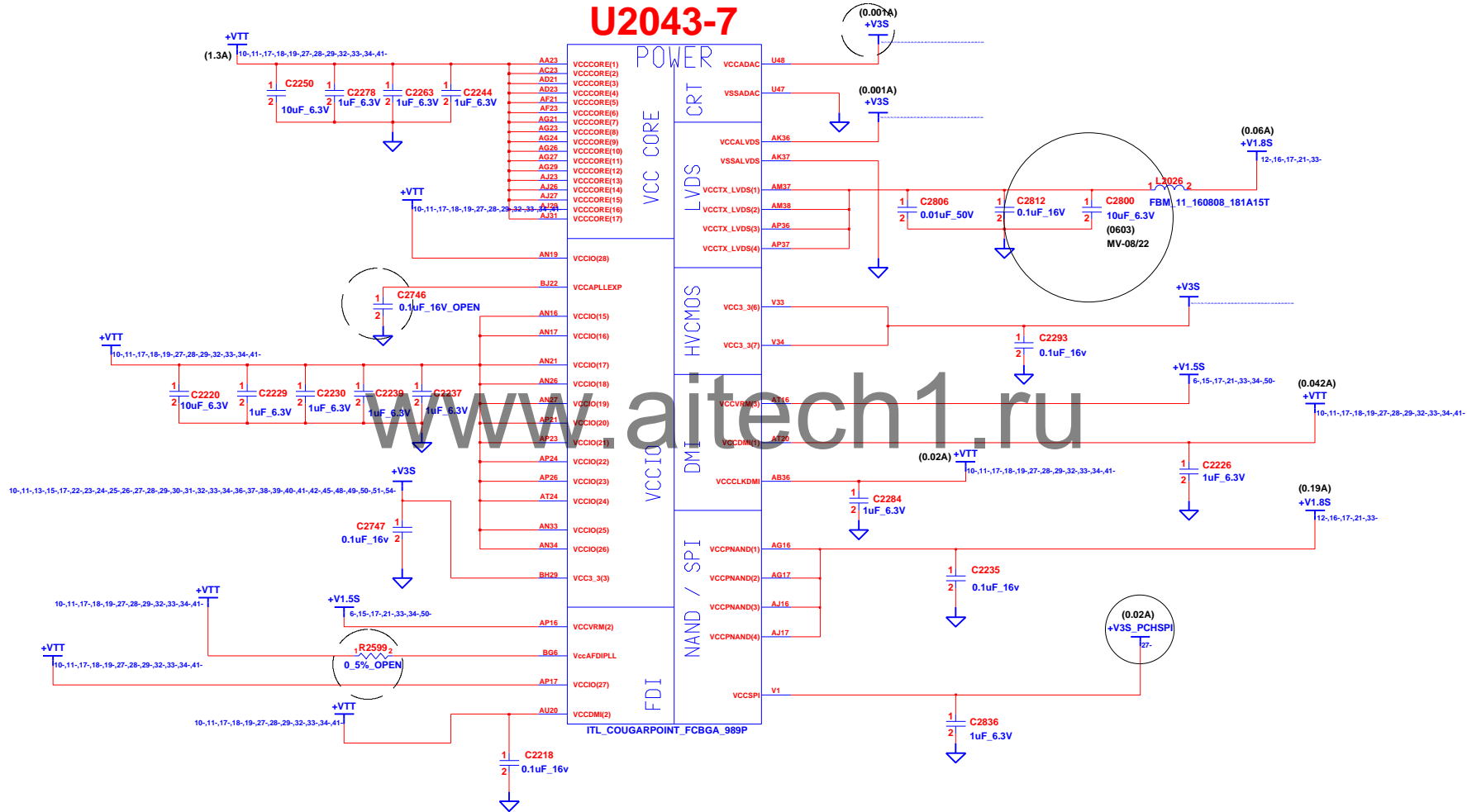
U2043-6



INVENTEC

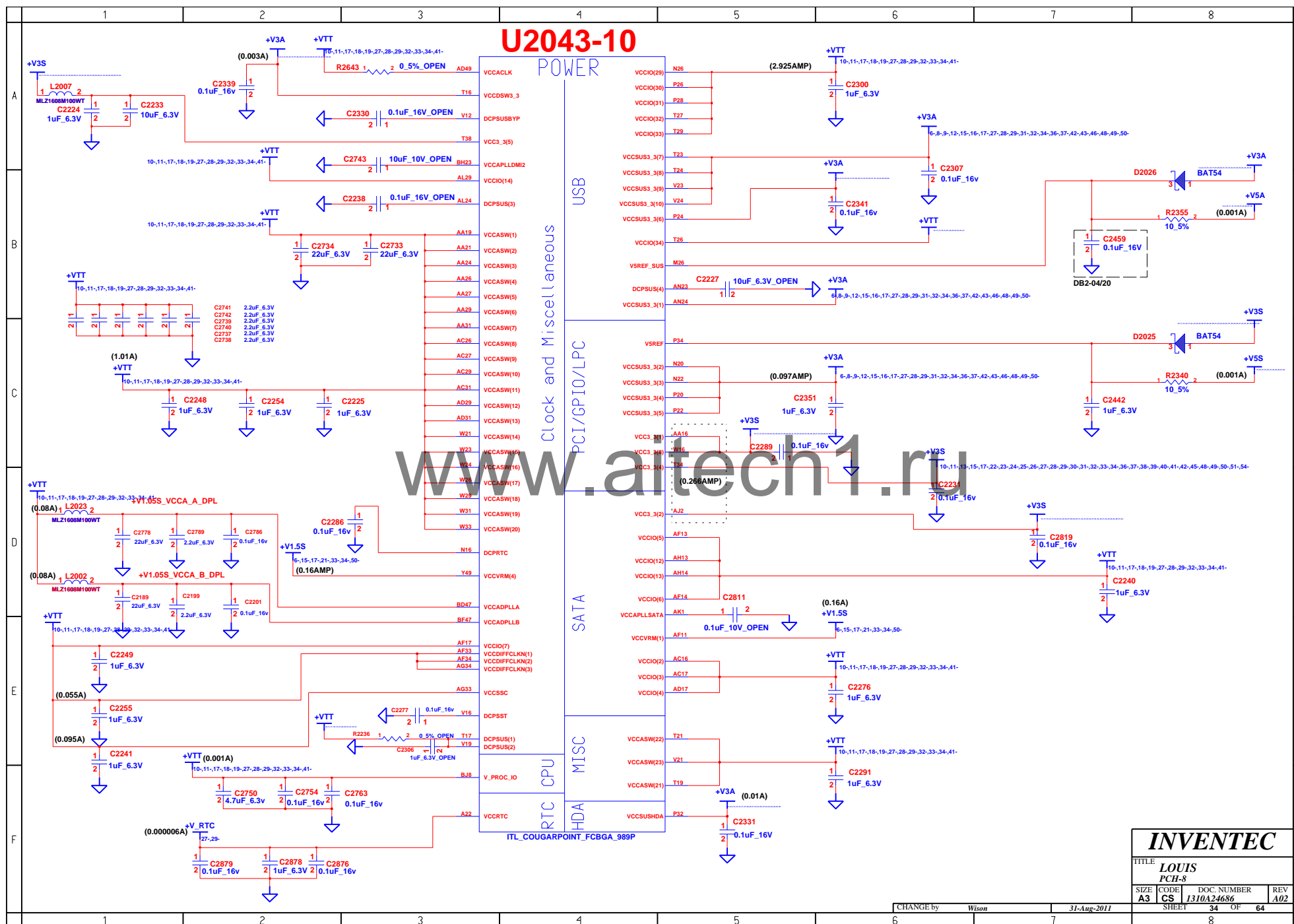
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SIZE A3	CODE CS	DOC. NUMBER 1310A24686	REV A02
CHANGE by Wilson		31-Aug-2011	
SHEET 32		OF 64	

U2043-7



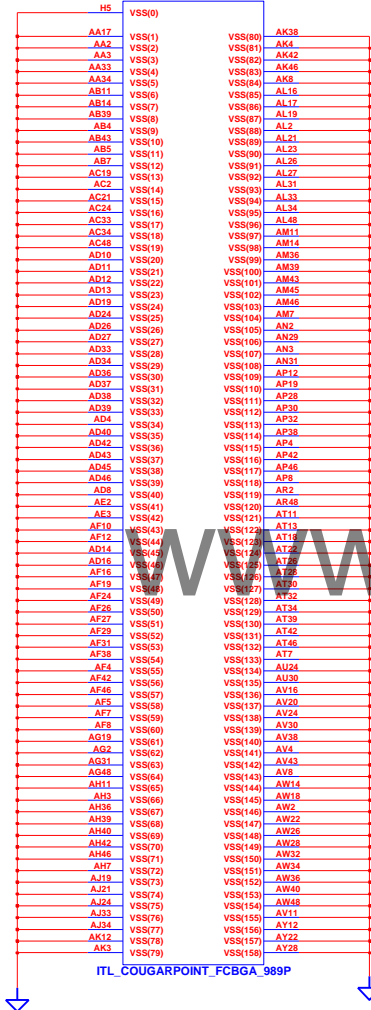
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CHANGE by Wison		31-Aug-2011	
SHEET 33		OF 64	

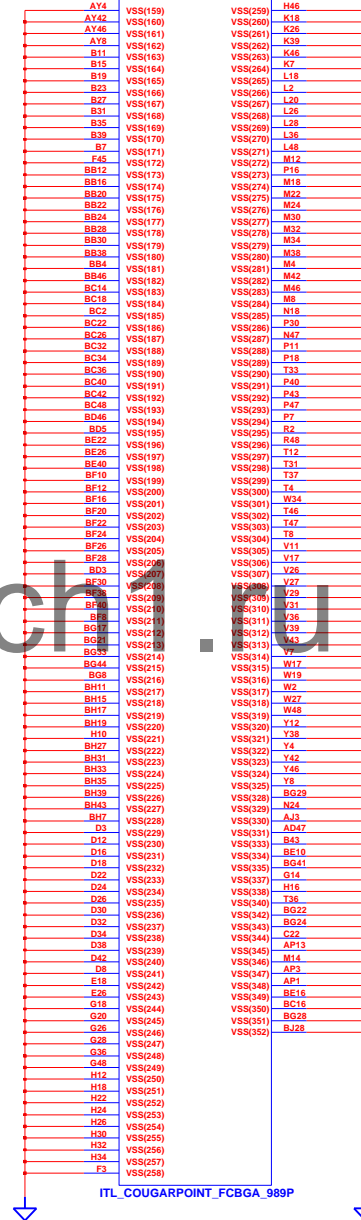


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B									B
C									C
D									D
E									E
F									F

U2043-8

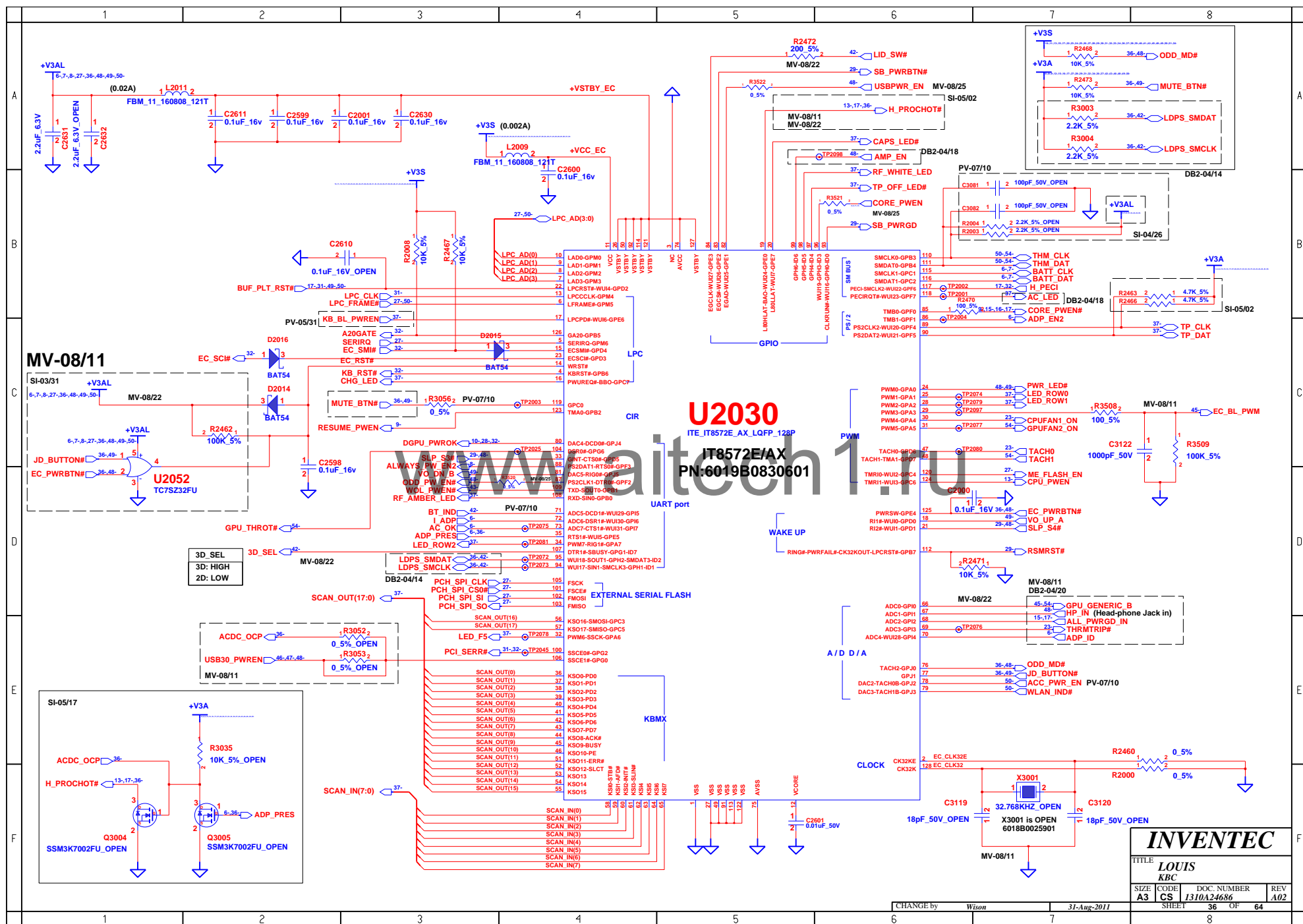


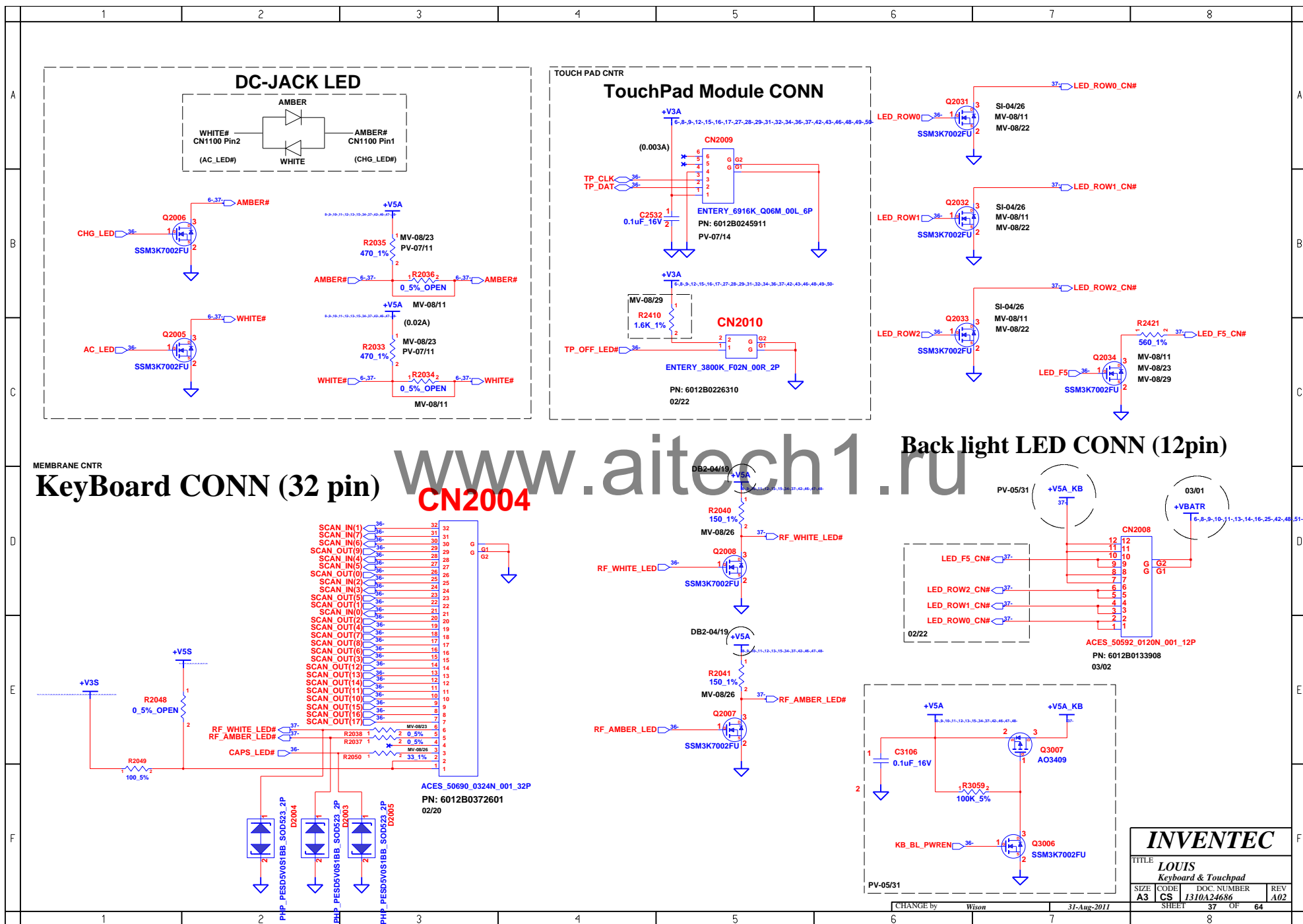
U2043-9



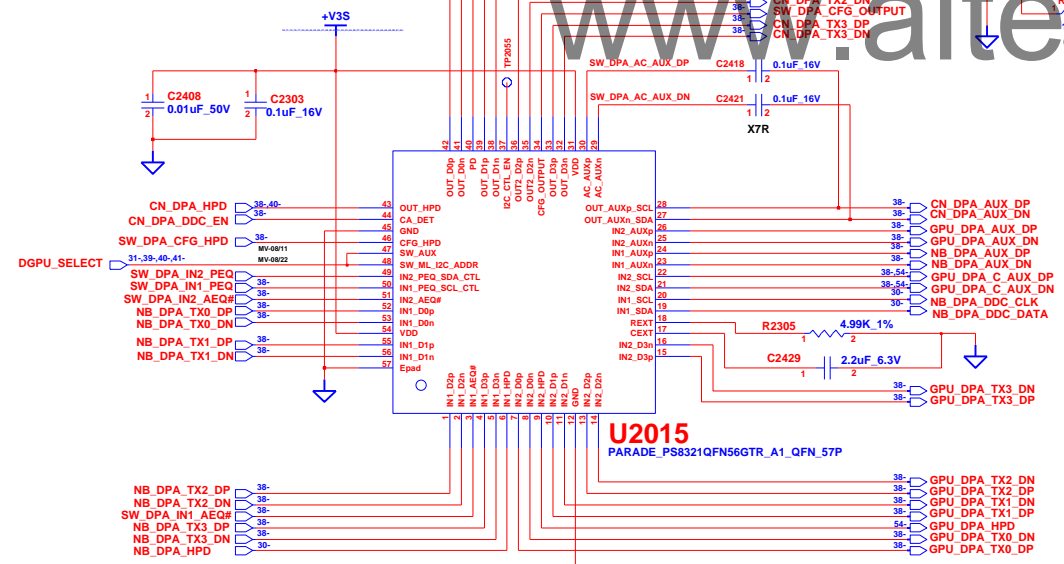
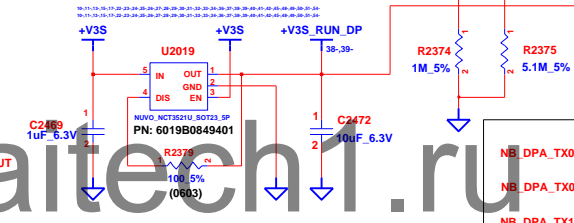
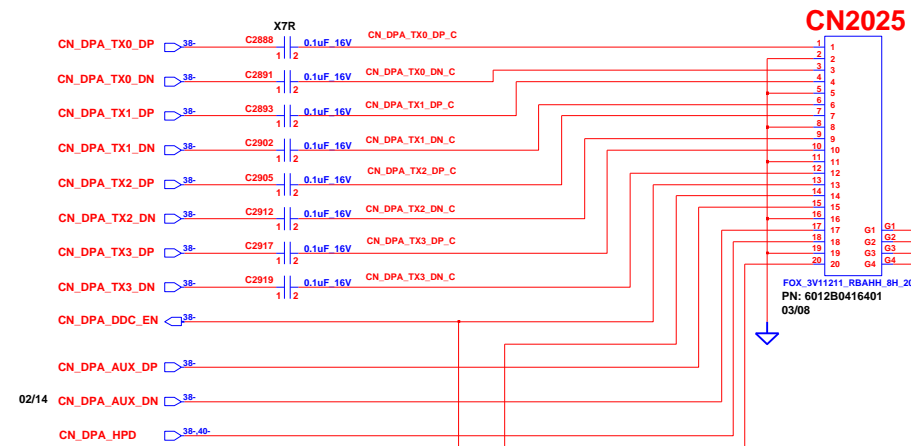
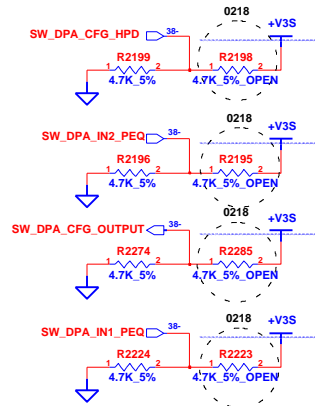
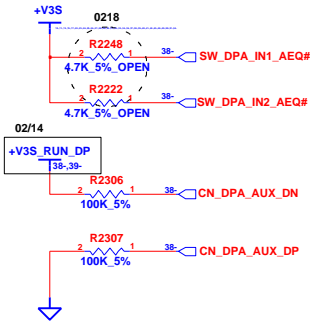
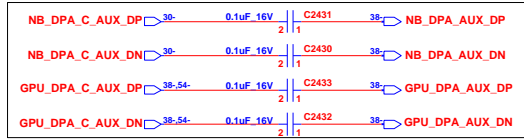
INVENTEC

TITLE LOUIS PCH-9			
SIZE A3	CODE CS	DOC. NUMBER 1310A24686	REV A02
SHEET 35 OF 64			



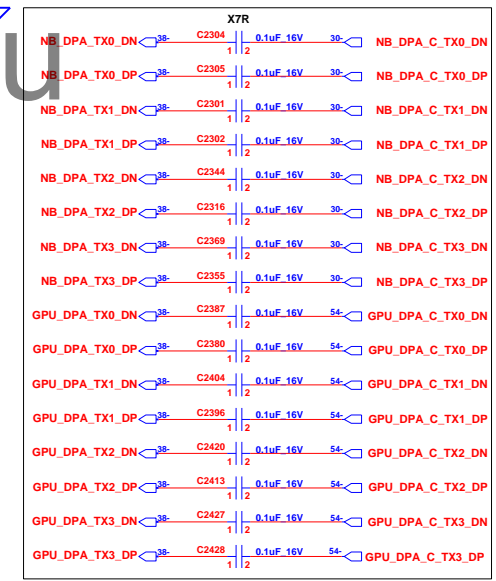


Display Port A



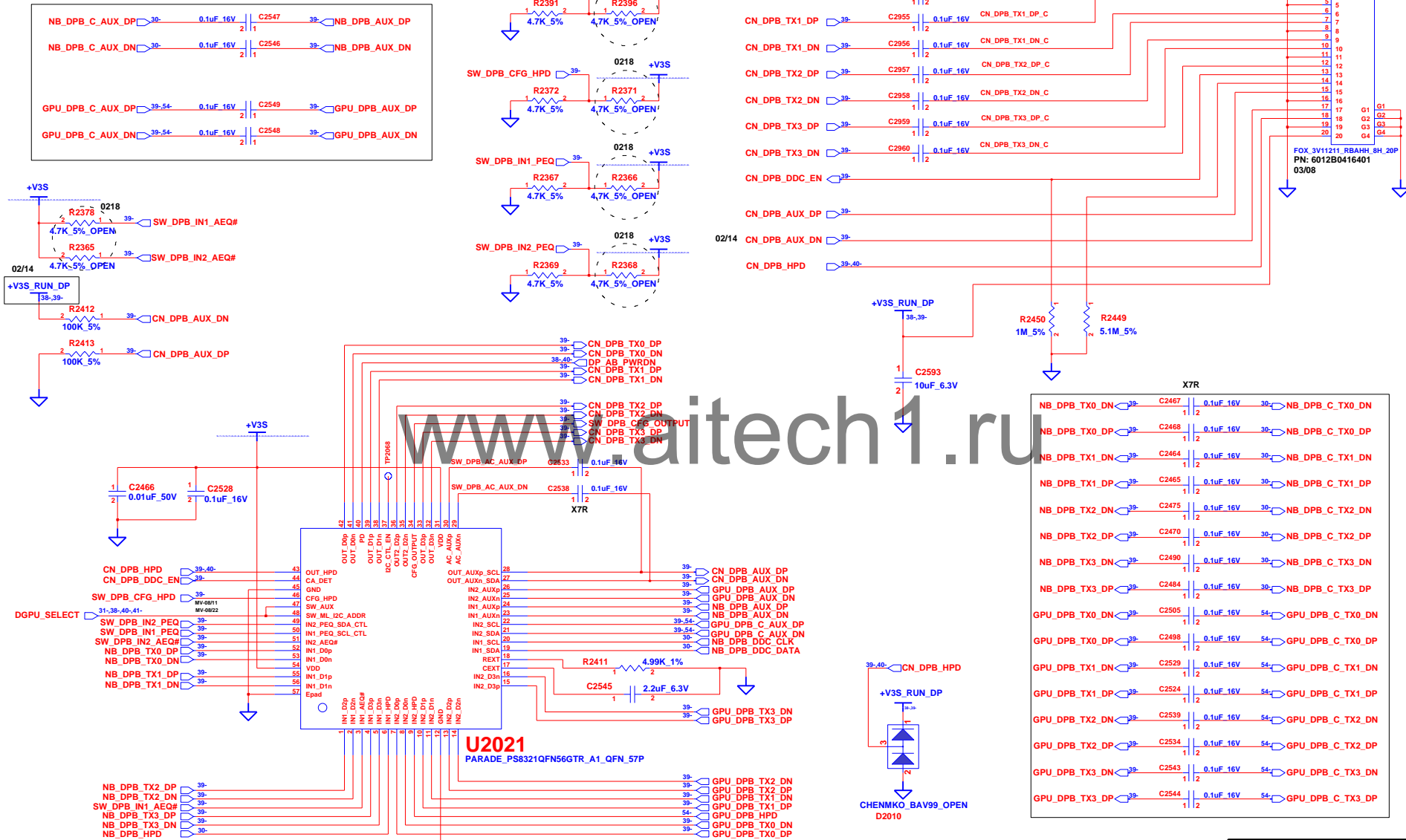
DGPU_SELECT
H : IN2 PORT IS SELECTED (GPU)
L : IN1 PORT IS SELECTED (NB)

SW_ML=L, port1 CA_DET=L, AUX
SW_AUX=L, port1 CA_DET=H, I2C
SW_ML=H, port2
SW_AUX=H, port2



INVENTEC			
TITLE LOUIS DisplayPort			
SIZE A3	CODE CS	DOC NUMBER 1310A24686	REV A02
SHEET 38		OF 64	

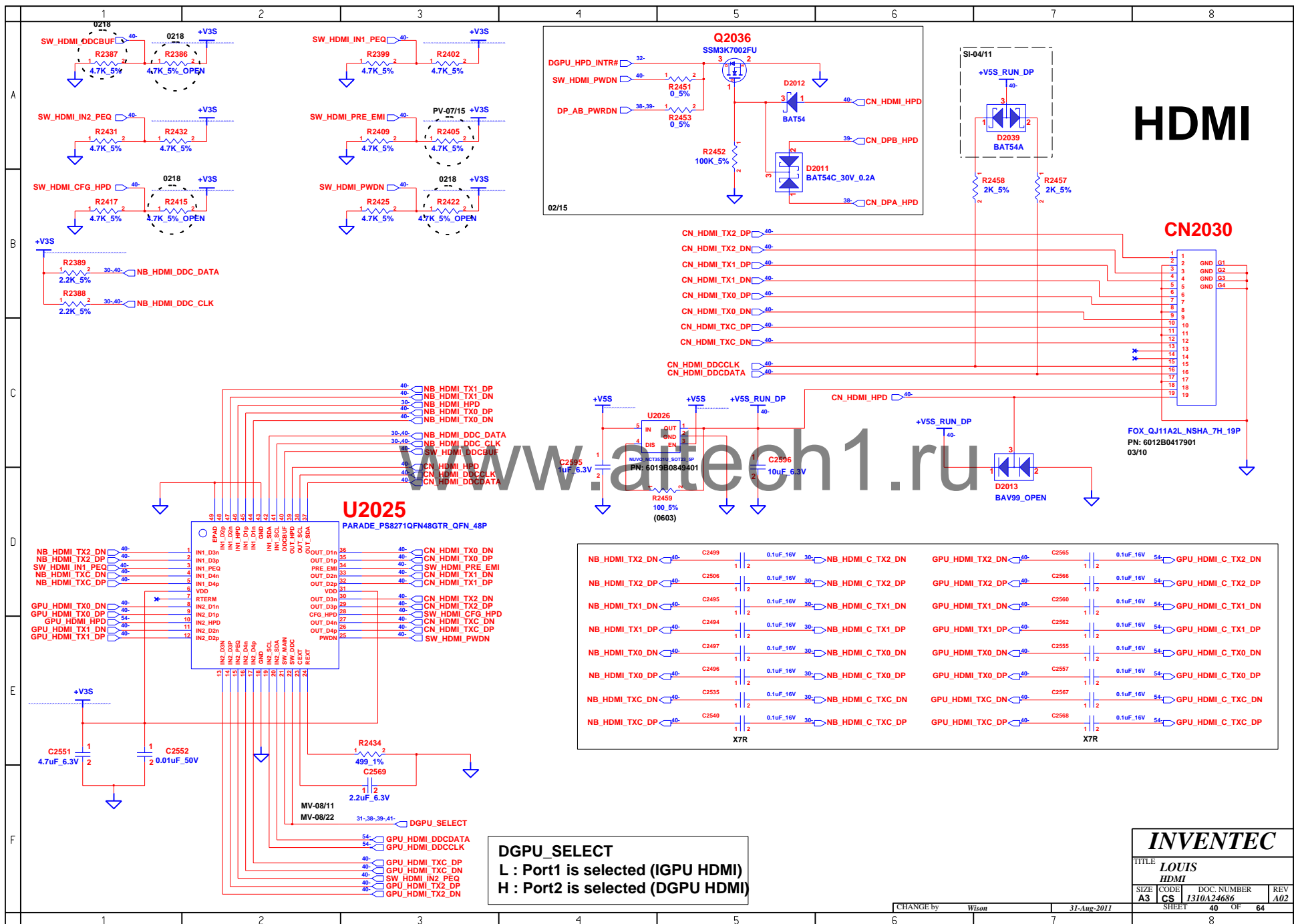
Display Port B

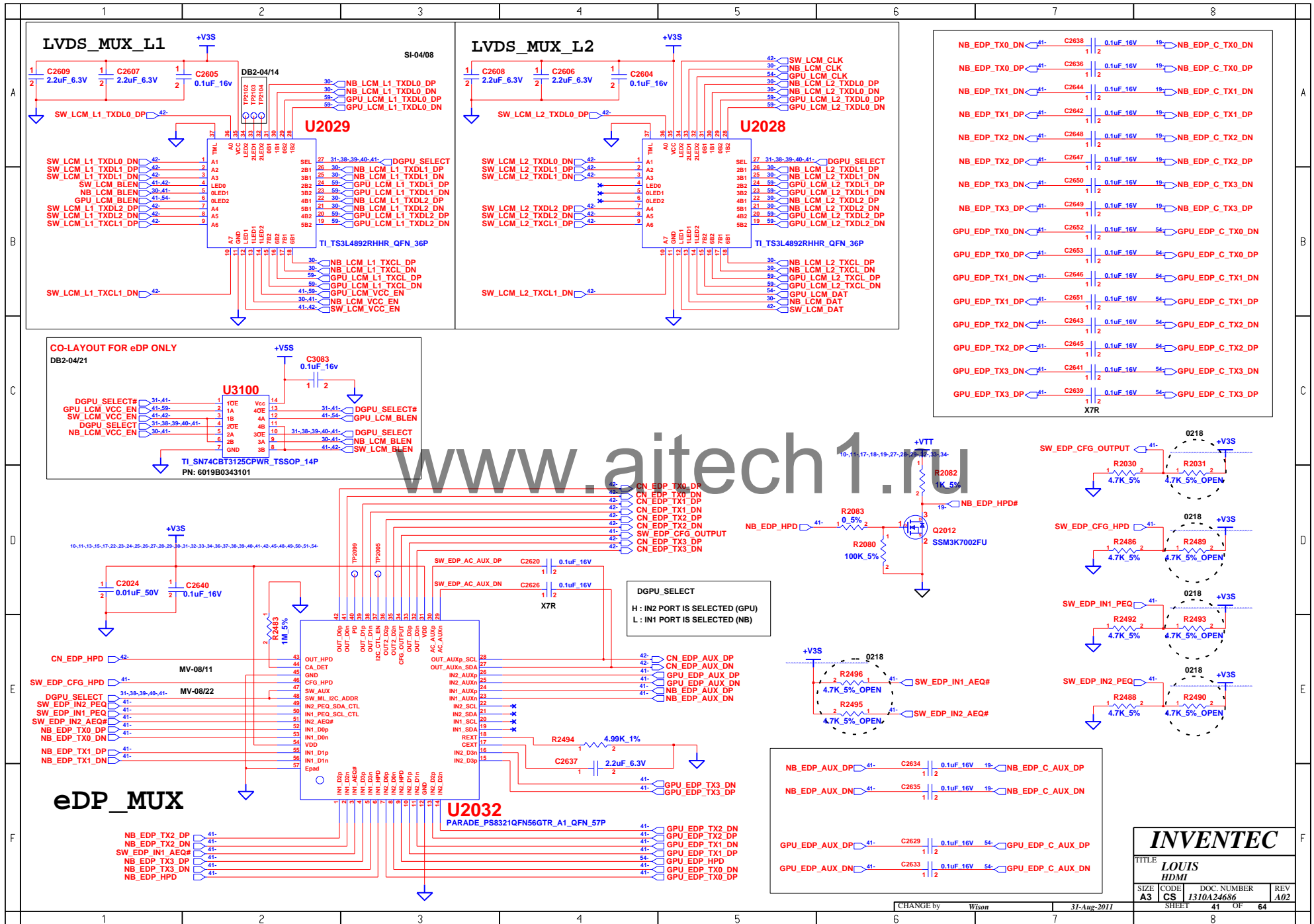


DGPU_SELECT
H: IN2 PORT IS SELECTED (GPU)
L: IN1 PORT IS SELECTED (NB)

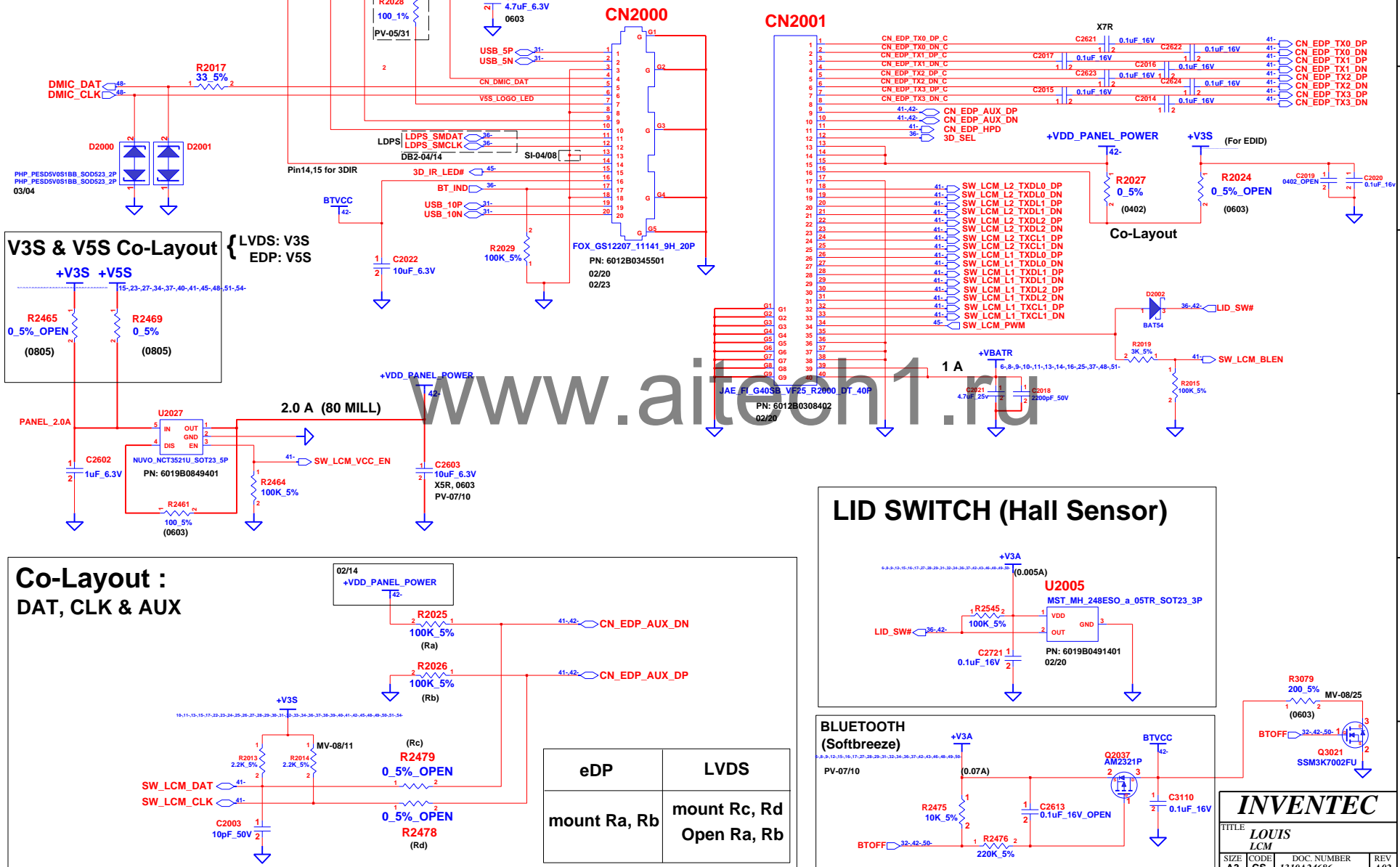
SW_ML=L, port1 CA_DET=L, AUX
SW_AUX=L, port1 CA_DET=H, I2C
SW_ML=H, port2
SW_AUX=H, port2

INVENTEC			
TITLE LOUIS DisplayPort			
SIZE A3	CODE CS	DOC. NUMBER 1310A24686	REV A02
CHANGE by Wilson		31-Aug-2011	
SHEET 39		OF 64	





LCM & eDP & BT CONN

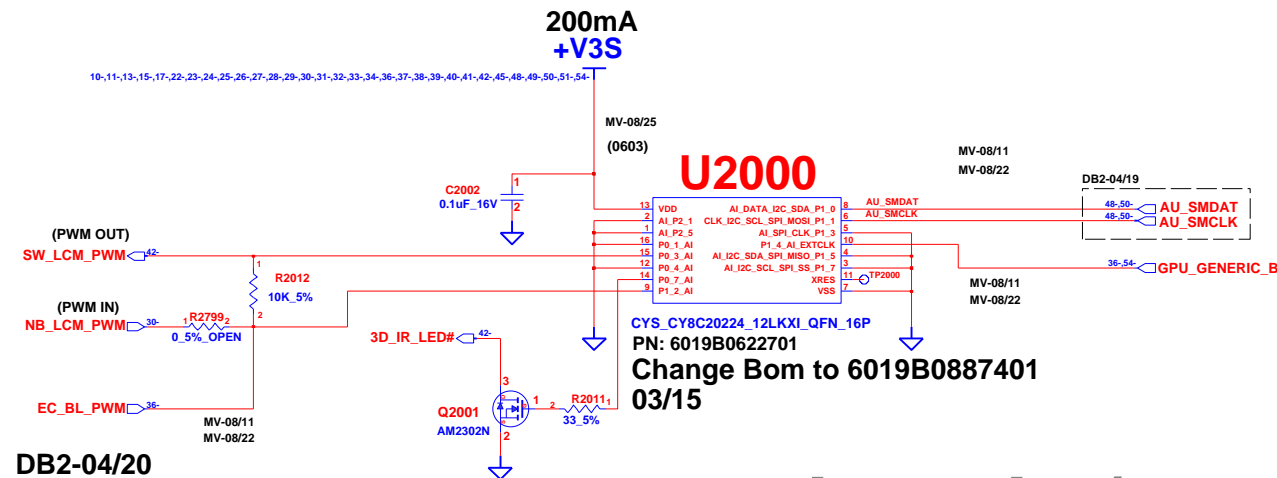


**INVENTEC**

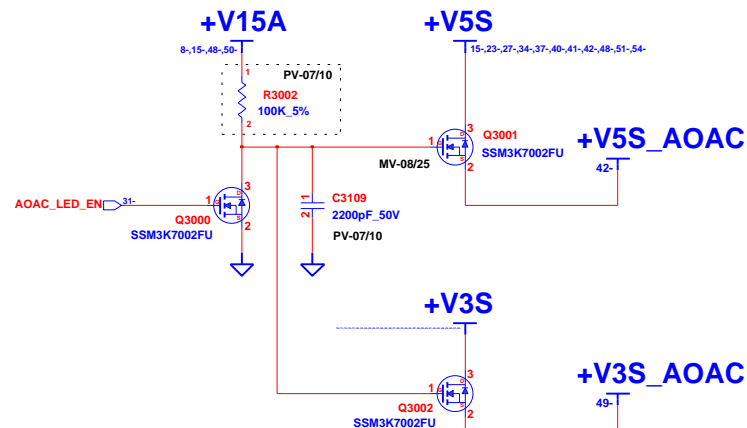
TITLE			
LOUIS RJ-45			
SIZE	CODE	DOC. NUMBER	REVISION
A3	CS	1310A24686	A0

CHANGE by	Wison	31-Aug-2011
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3D PWM Control



AOAC POWER

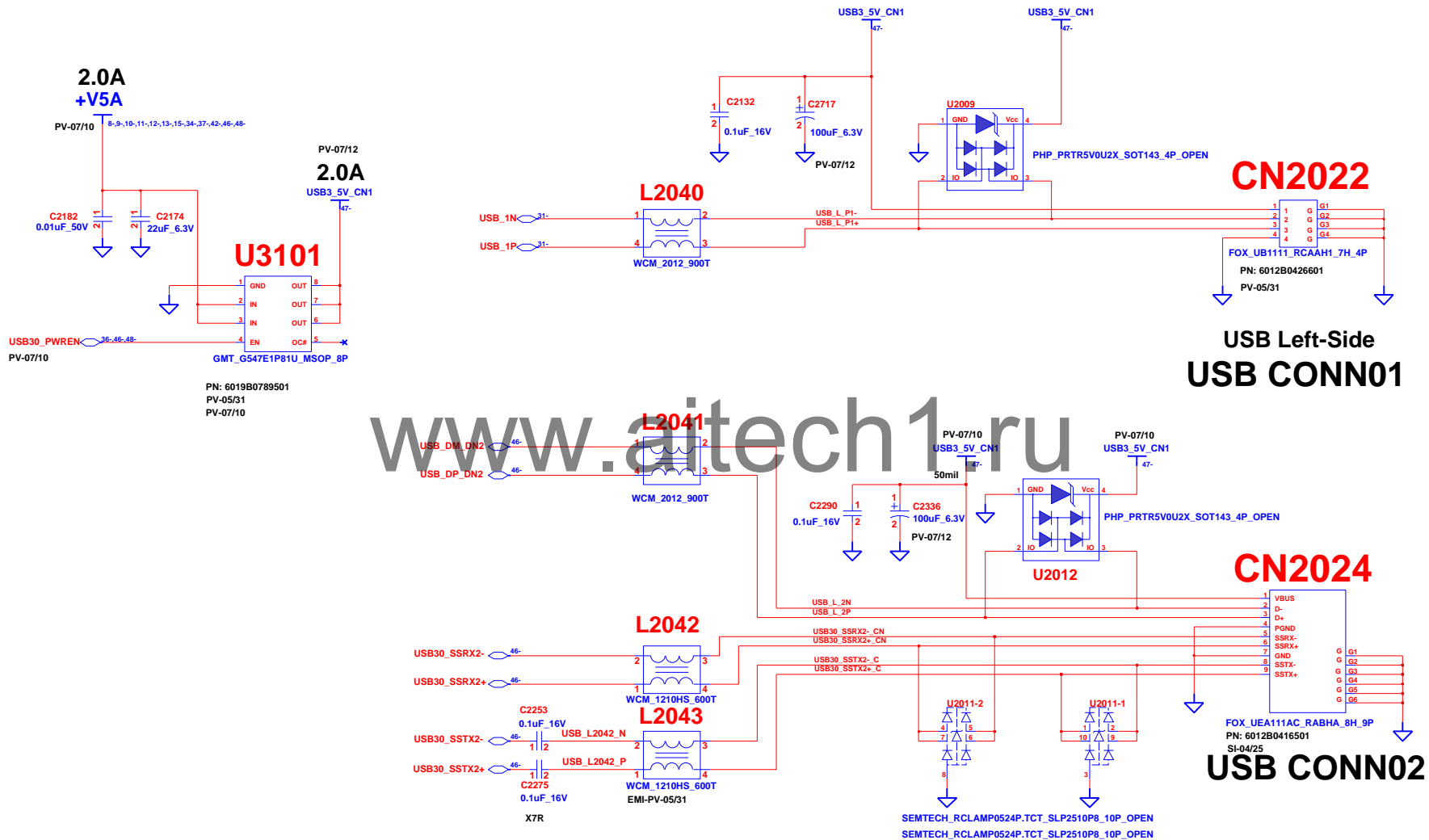


INVENTEC

TITLE LOUIS CARD READER			
SIZE A3	CODE CS	DOC. NUMBER 1310A24686	REV A02
SHEET 45		OF 64	

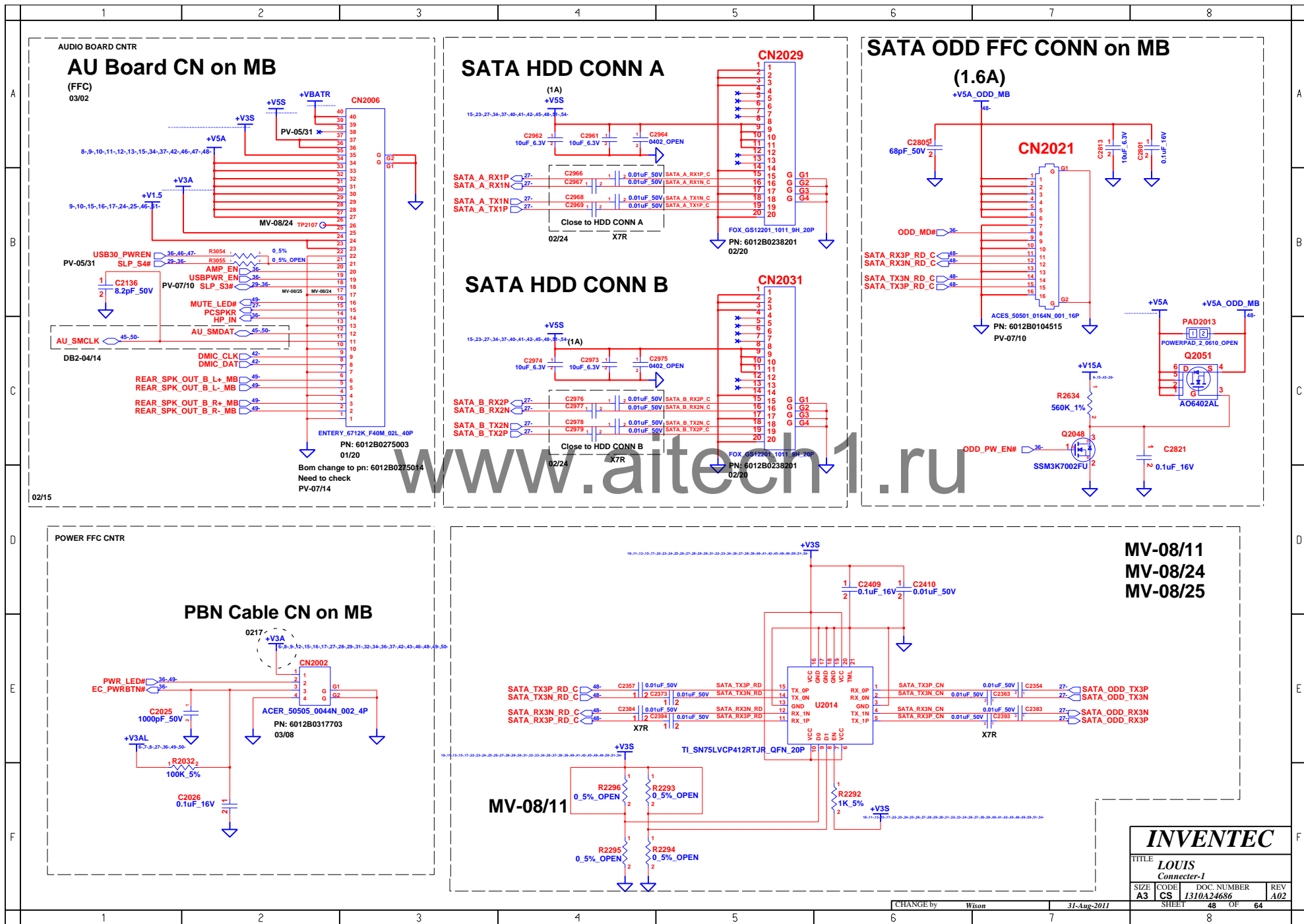
CHANGE by Wison 31-Aug-2011

USB 2.0 / 3.0 CN

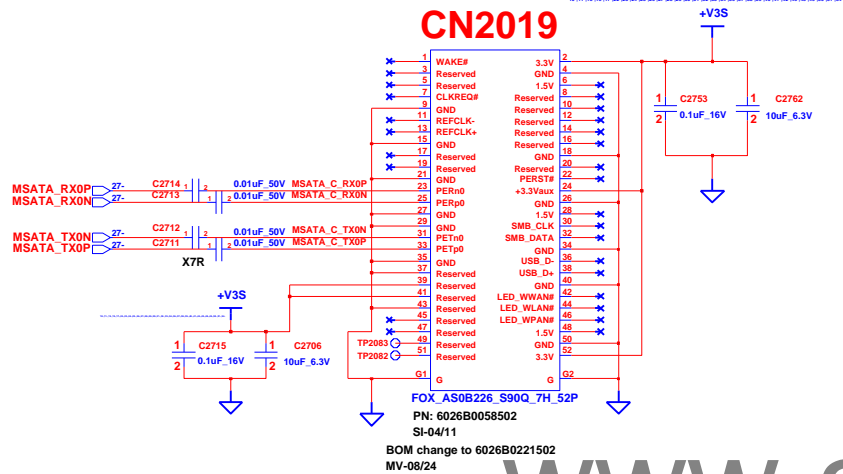


INVENTEC

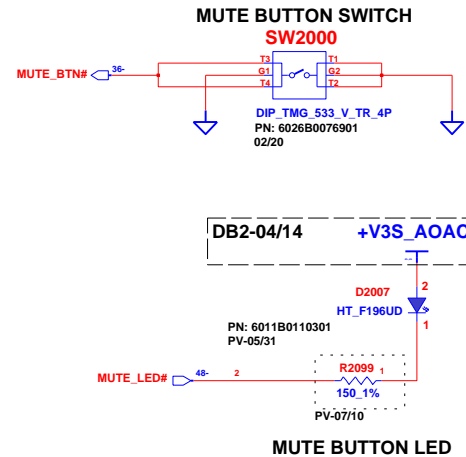
TITLE LOUIS USB 3.0			
SIZE A3	CODE CS	DOC. NUMBER 1310A24686	REV A02
CHANGE by Wilson		31-Aug-2011	
SHEET 47		OF 64	



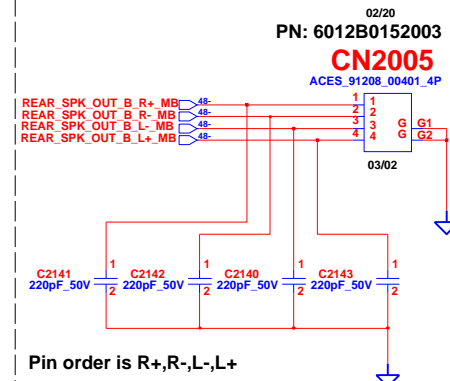
m-SATA Board CN on MB



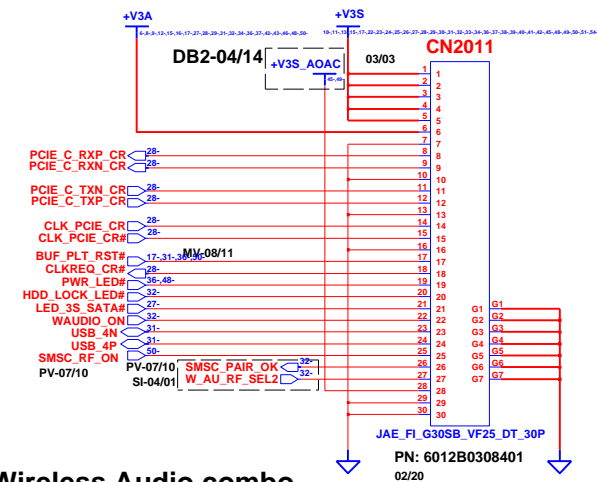
MUTE Board CN on MB



REAR SPEAKER CNTR ON MB

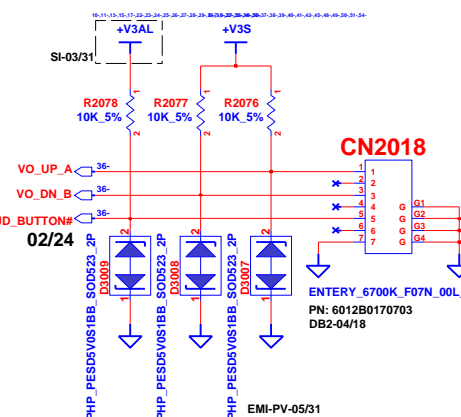


CR Board CN on MB



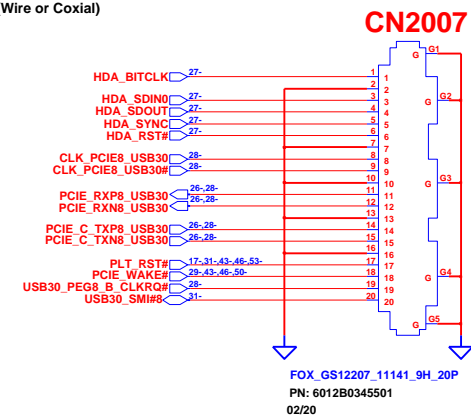
Wireless Audio combo

JOG DIAL CN on MB



AU-USB30 CN ON MB

(Wire or Coaxial)



INVENTEC

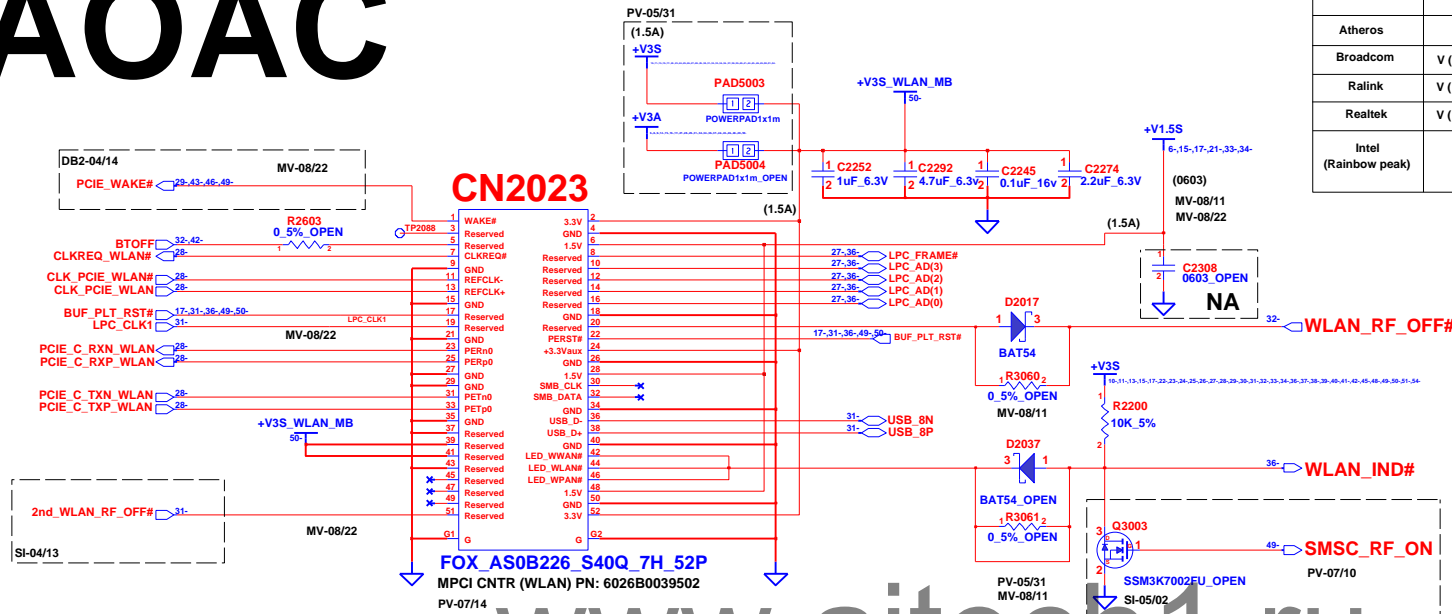
TITLE			
LOUIS			
Connector-2			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	1310A24686	A02
SHEET		49	OF 64

CHANGE by Wilson 31-Aug-2011

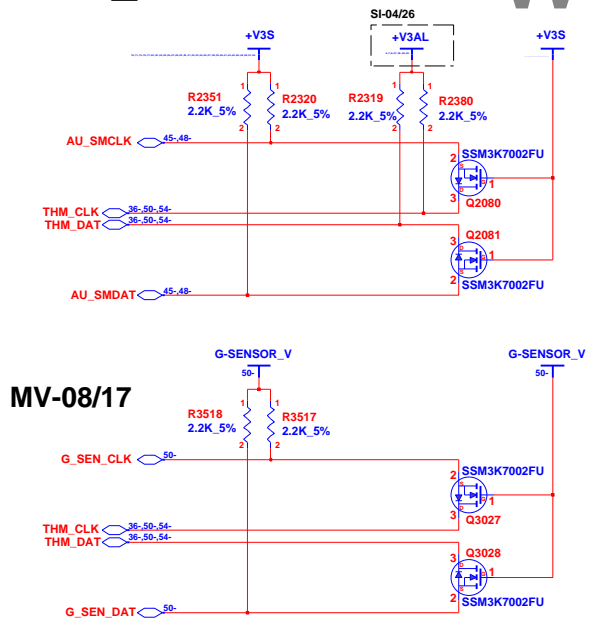
AOAC

WLAN CONN (MINICARD)

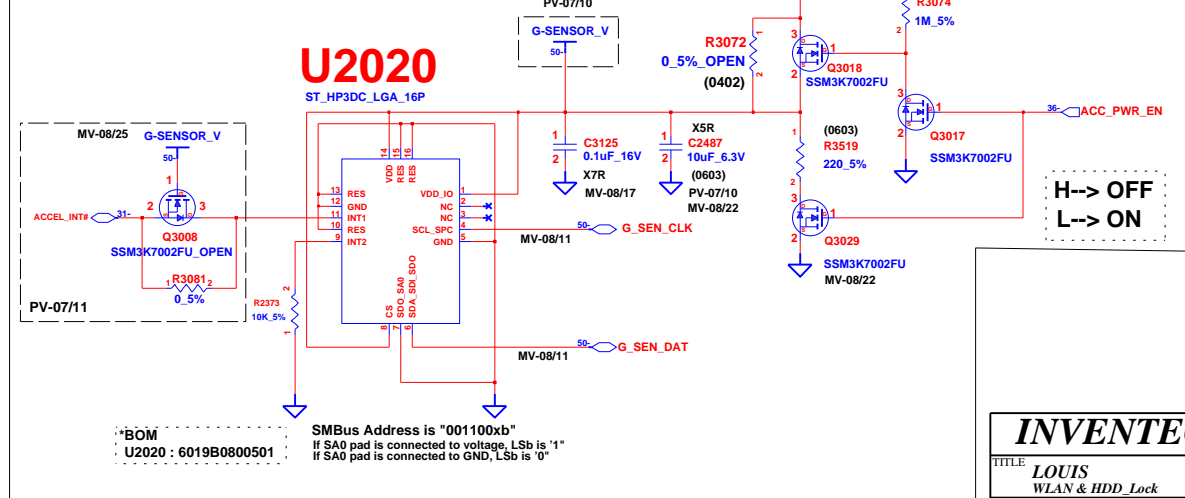
	Pin 5	Pin 19	Pin 51
Atheros			V (Reserved)
Broadcom	V (Reserved)		
Ralink	V (Reserved)		
Realtek	V (Reserved)		
Intel (Rainbow peak)		V (ES2 sample / WW27)	V (QS sample/WW42)



AUB_SMBUS



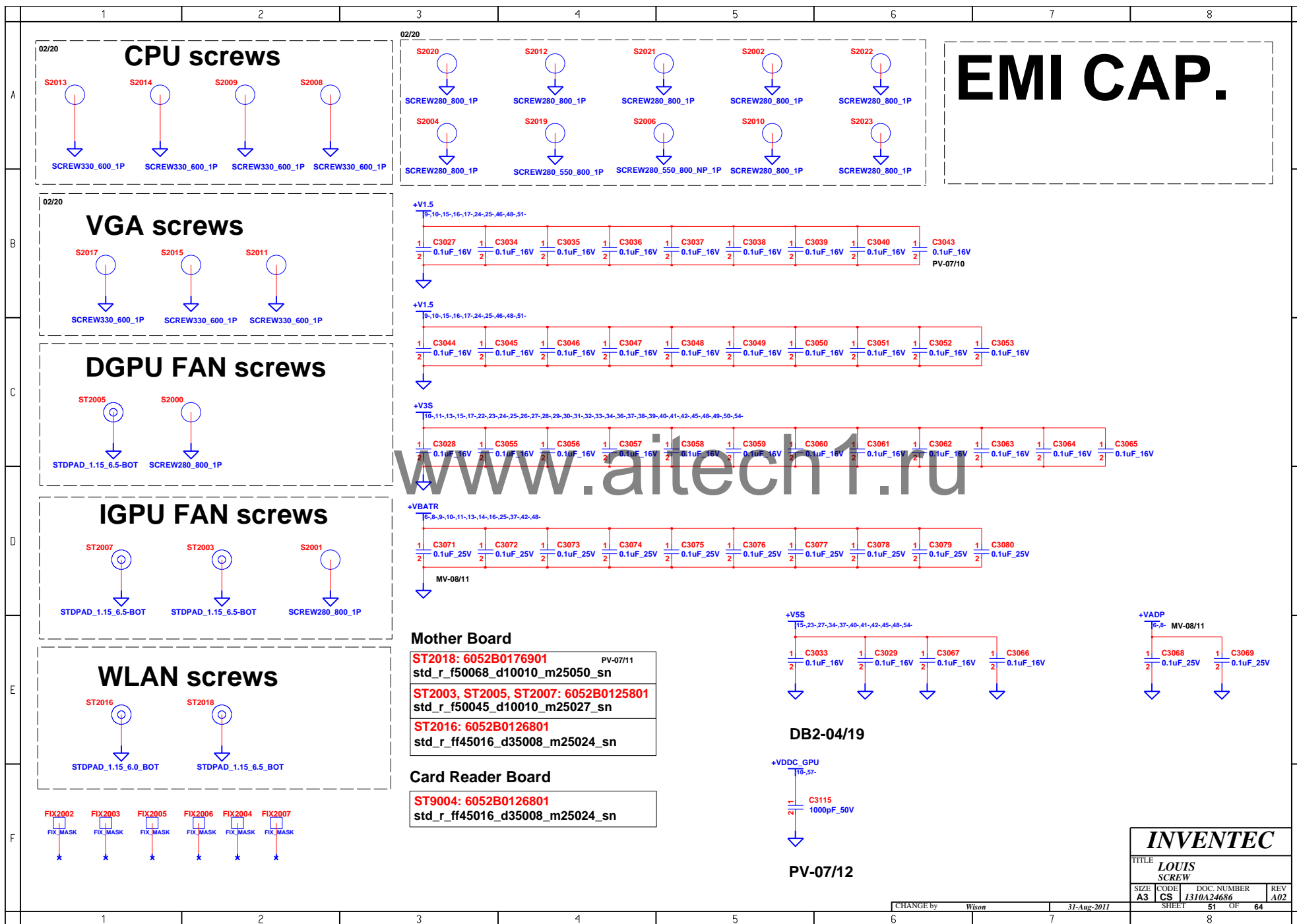
HARDDRIVE PROTECTION



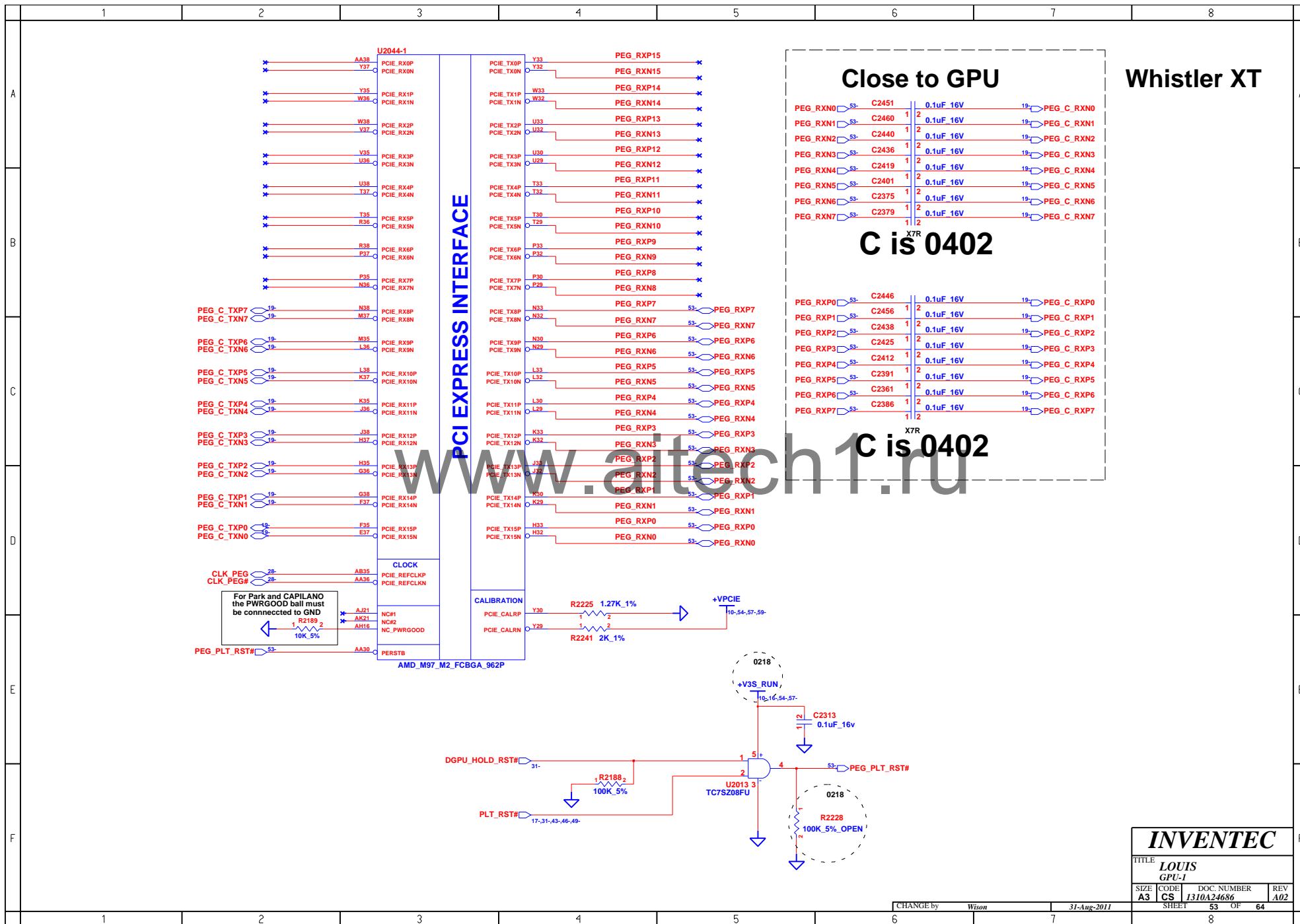
INVENTEC

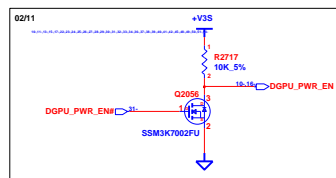
TITLE: LOUIS WLAN & HDD Lock			
SIZE A3	CODE CS	DOC. NUMBER 1310A24686	REV A02
SHEET 50		OF 64	

CHANGE by: Wilson 31-Aug-2011



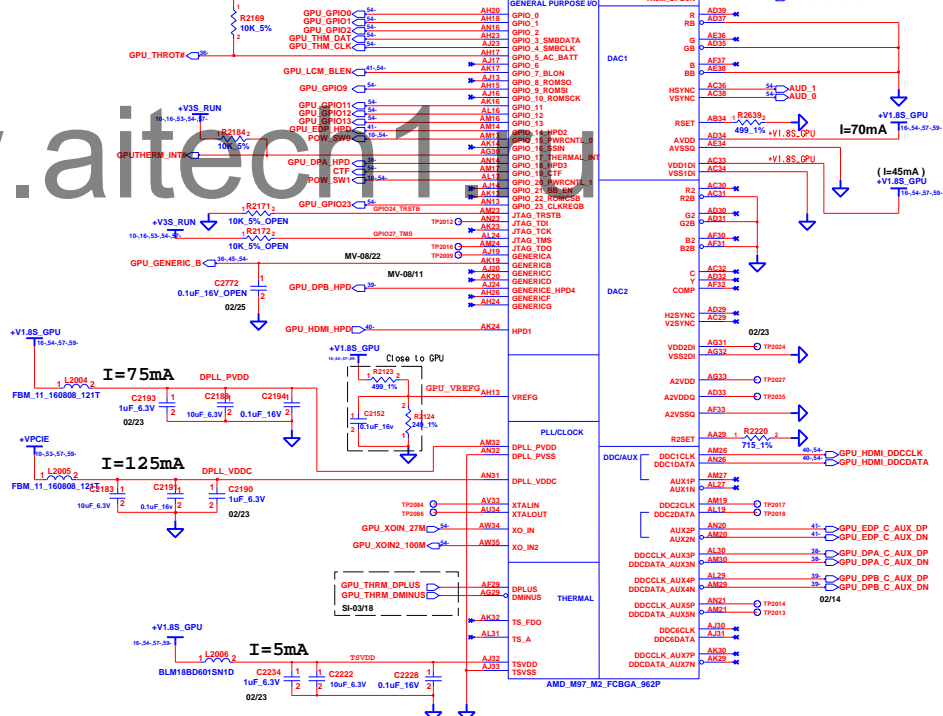
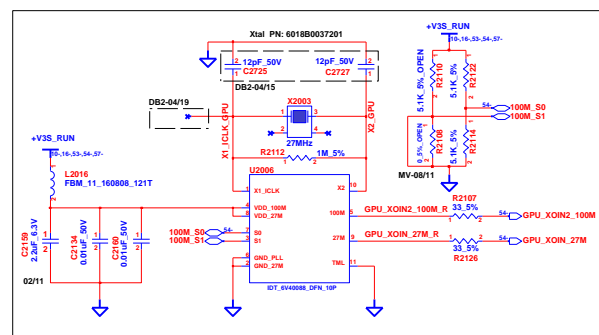
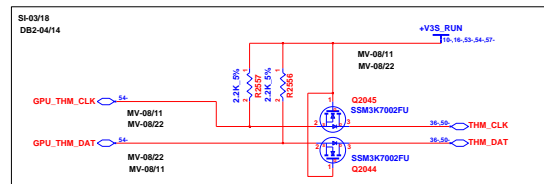
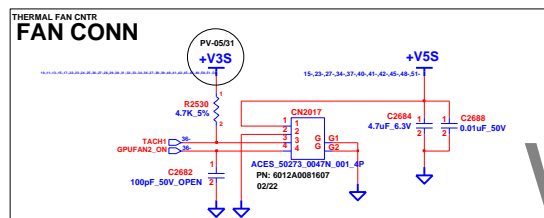
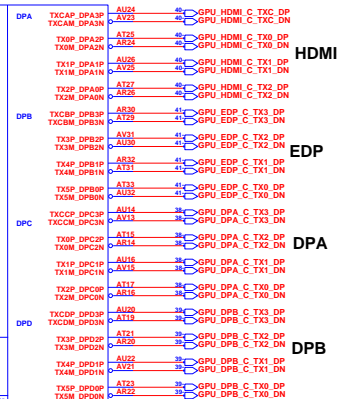
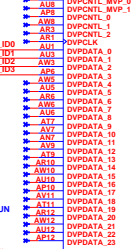
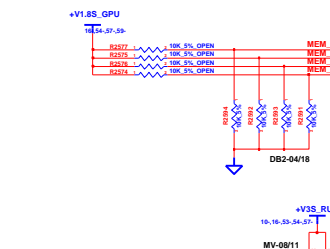
	1	2	3	4	5	6	7	8	
A	PV-07/13				GPU				A
	VRAM BOM Change								
B	Supplier	VRAM	Density	Speed	Version	Supplier PN	IEC PN		B
	Samsung	GDDR5	32Mx32	N/A	G die,35nm	K4G10325FG-HC04	6019B0889201		
C	Supplier	VRAM	Density	Speed	Version	Supplier PN	IEC PN		C
	Hyinx	GDDR5	32Mx32	N/A	B die,44nm	H5GQ1H24BFR-T2C	6019B0866701		
D	Supplier	VRAM	Density	Speed	Version	Supplier PN	IEC PN		D
	Samsung	GDDR5	64Mx32	N/A	N/A	K4G20325FC-HC04	6019B0842201		
E	Supplier	VRAM	Density	Speed	Version	Supplier PN	IEC PN		E
	Hyinx	GDDR5	64Mx32	N/A	N/A	H5GQ2H24MFR-T2C	6019B0843001		
F	Supplier	VRAM	Density	Speed	Version	Supplier PN	IEC PN		F
	Alpida	GDDR5	64Mx32	N/A	N/A	EDW2032BABG-50-F	6019B0842301		
						<div>INVENTEC</div> <div> <div>TITLE</div> <div>LOUIS FAN</div> <div> <div>SIZE</div> <div>A3</div> <div>CODE</div> <div>CS</div> <div>DOC. NUMBER</div> <div>1310A24686</div> <div>REV</div> <div>A02</div> </div> </div> <div> <div>CHANGE by</div> <div>Wison</div> <div>31-Aug-2011</div> <div>SHEET</div> <div>52</div> <div>OF</div> <div>64</div> </div>			
	1	2	3	4	5	6	7	8	





If GPIO_22_EN = 0, then GPIO[13:11] defines the primary memory aperture size.			
GPIO_13	GPIO_12	GPIO_11	Memory aperture size
0	0	1	2GB / 1GB (Default)
1	1	0	Reserved

MEM_ID3	MEM_ID2	MEM_ID1	MEM_ID0	Vendor
0	0	1	1	Hynix (1GB)
0	0	0	1	Samsung(1GB)
0	0	1	0	Hynix (2GB)
0	0	0	0	Samsung(2GB) (Default)
0	1	0	0	Alpida (2GB)



Engineer	COMPANY NAME	
Witness E&E	Address	
Drawn by	City	
Witness	TITLE	Size A2
R&D CHK	LOUIS	
DWG CTRL CHK	GPU-2	
MFG ENGR CHK		

Channel A

U2044-3

MEMORY INTERFACE A

www.aitech1.ru

DDR3/GDDR3Memory Stuff Option

	GDDR5	GDDR3	DDR3
MVDDQ	1.5V	1.8V/1.5V	1.5V
Ra	40.2R	40.2R	40.2R
Rb	100R	100R	100R

FOR M97,Broadway,Madiso and Park ONLY

INVENTEC

TITLE
LOUIS
GPU-3

SIZE A3	CODE CS	DOC. NUMBER 1310A24686	REV A02
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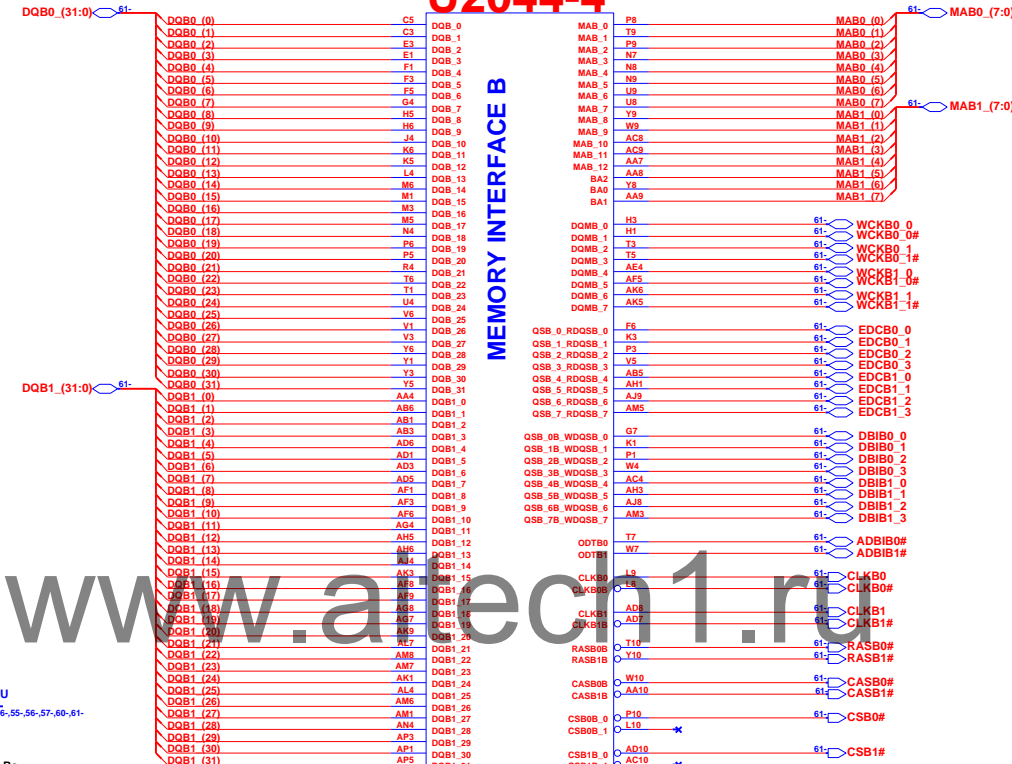
CHANGE by Wilson 31-Aug-2011

SHEET 55 OF 64

U2044-4

MEMORY INTERFACE B

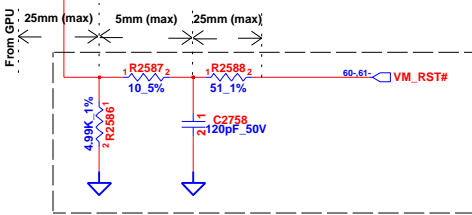
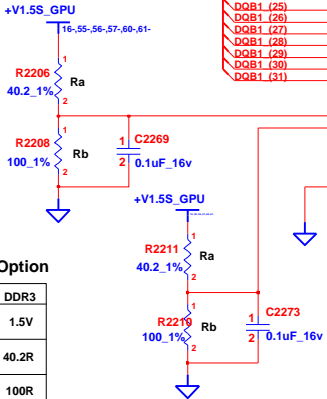
Channel B



www.aec1.com

DDR3/GDDR3Memory Stuff Option

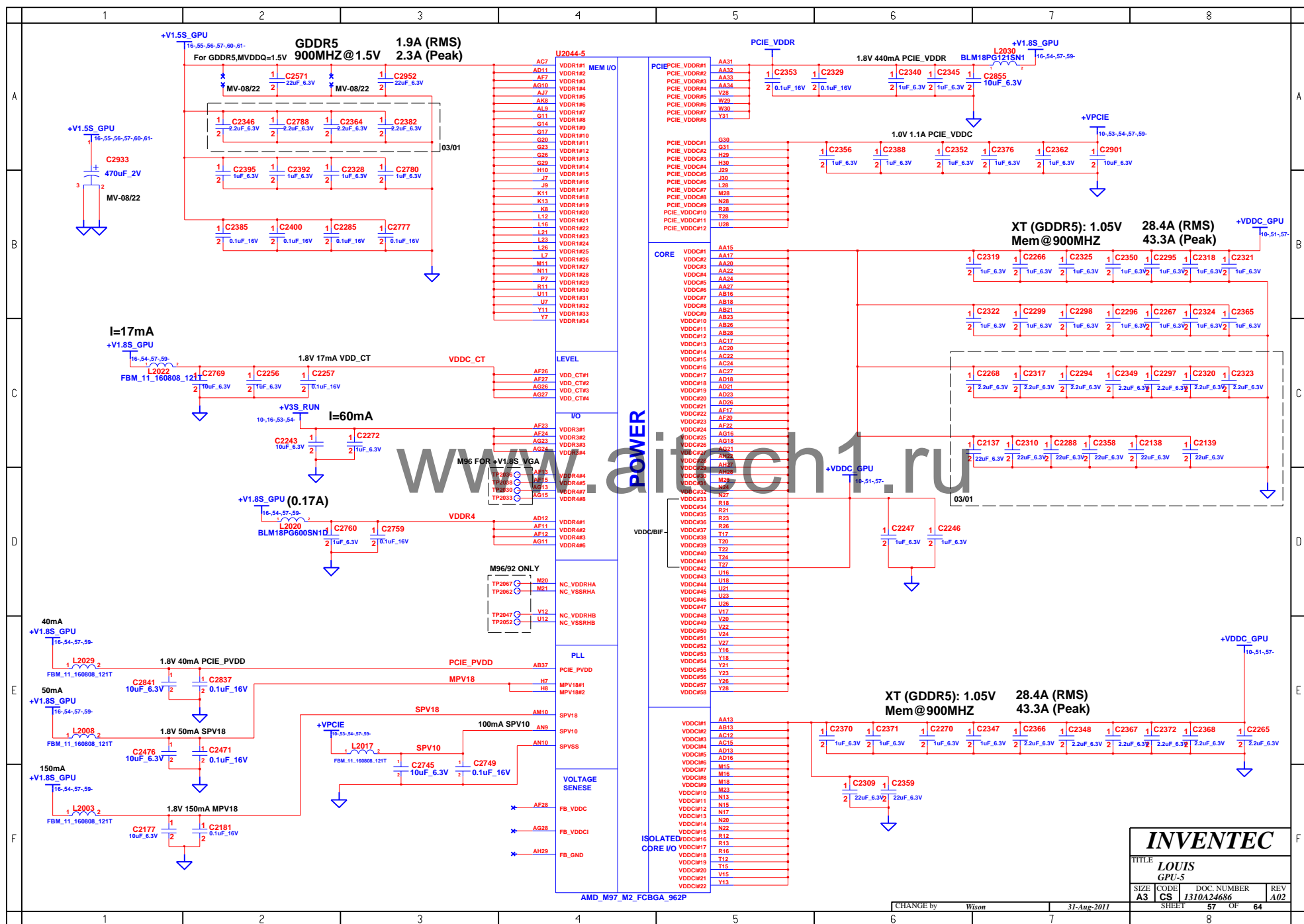
	GDDR5	GDDR3	DDR3
MVDDQ	1.5V	1.8V/1.5V	1.5V
Ra	40.2R	40.2R	40.2R
Rb	100R	100R	100R

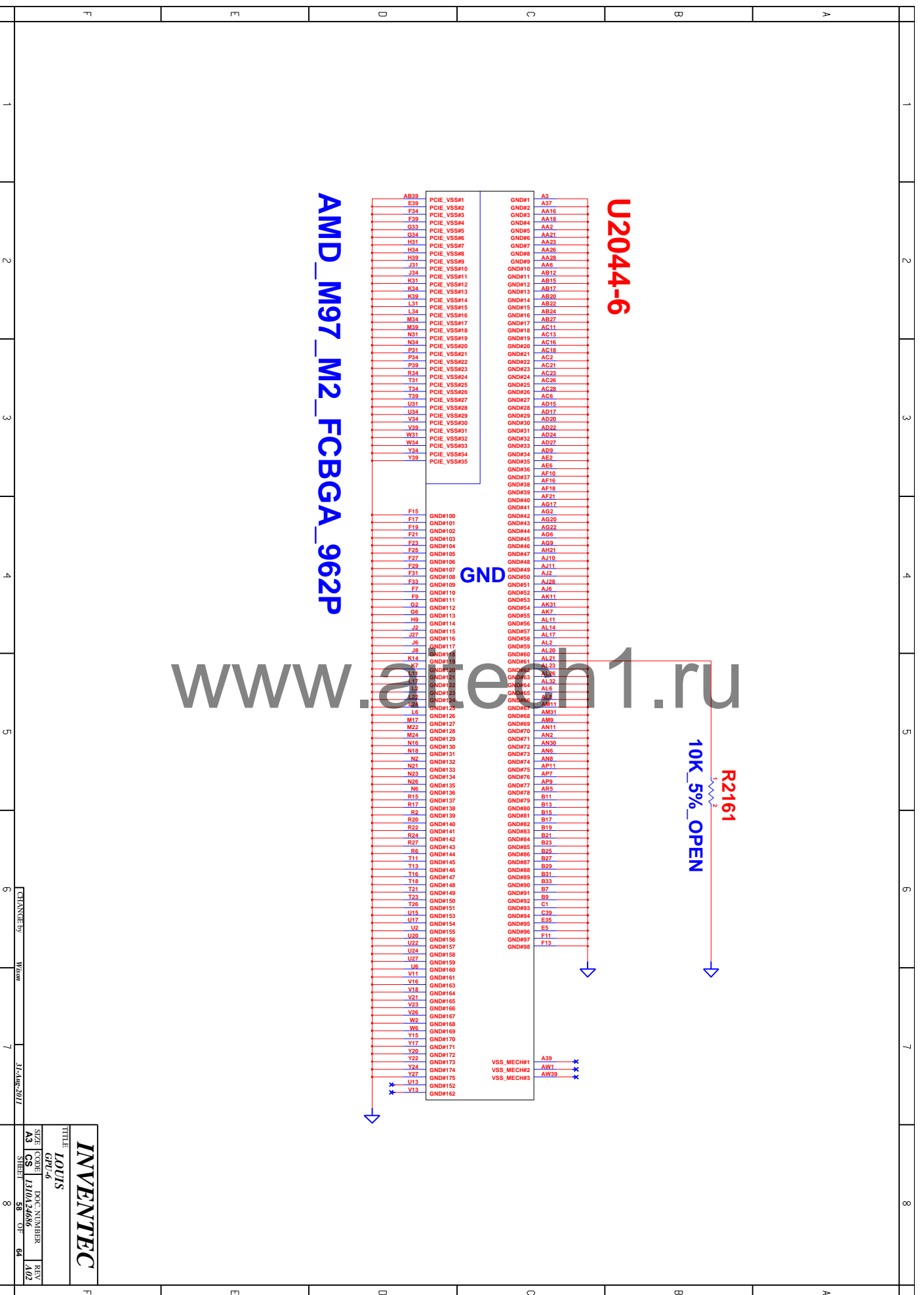


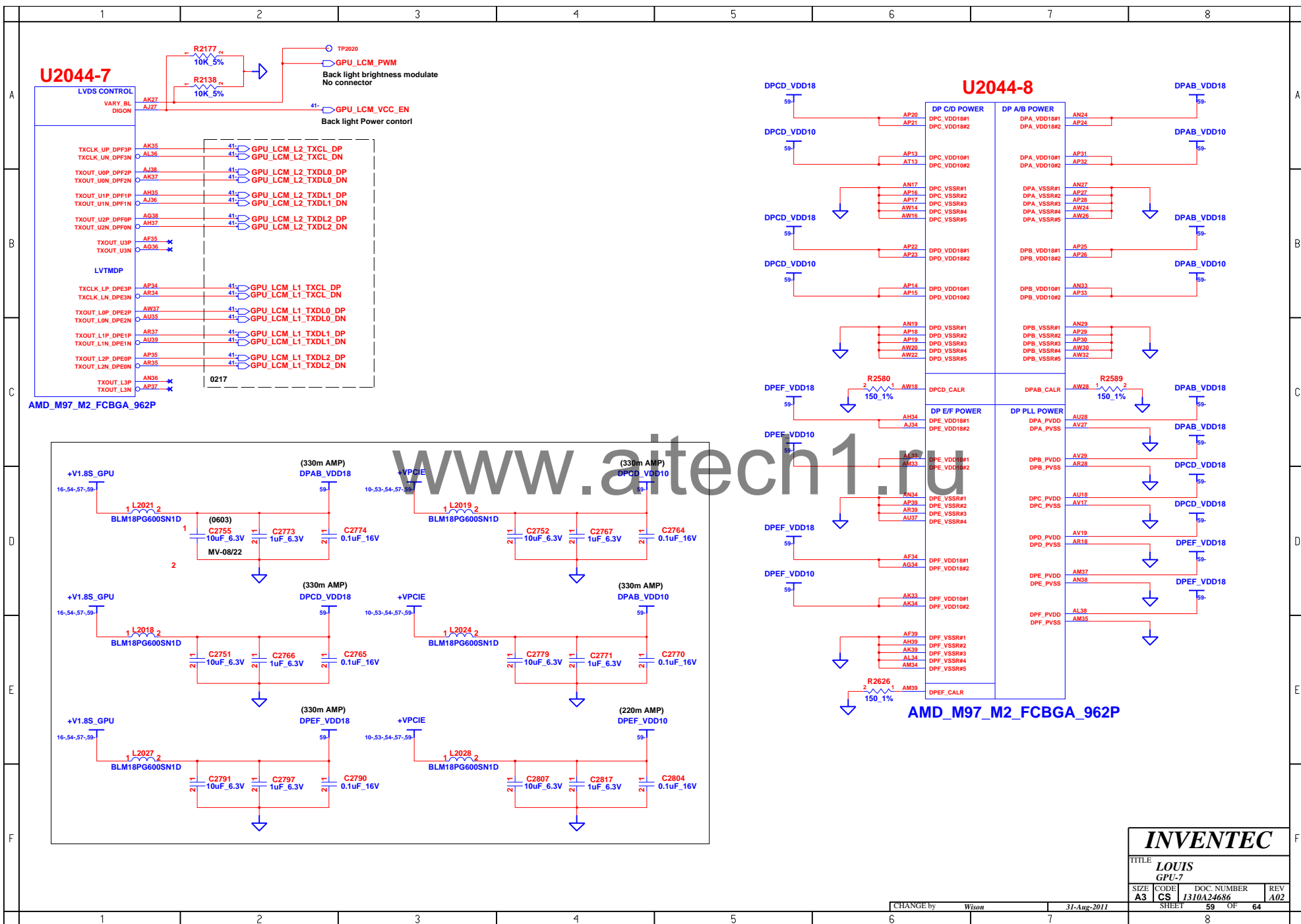
Place all these components vry close to GPU (Within 25mm)
and keep all component close to each other (within 5mm) except Rser2
** This basic topology should be used for DRAM_RST for DDR3/GDDR3/GDDR5
These Capacitors and Resistor values are an example only.
The Series R and || Cap values will depend on the DRAM load and will have to be
calculated for different Memory , DRAM Load and board to pass Reset Signal Spec.

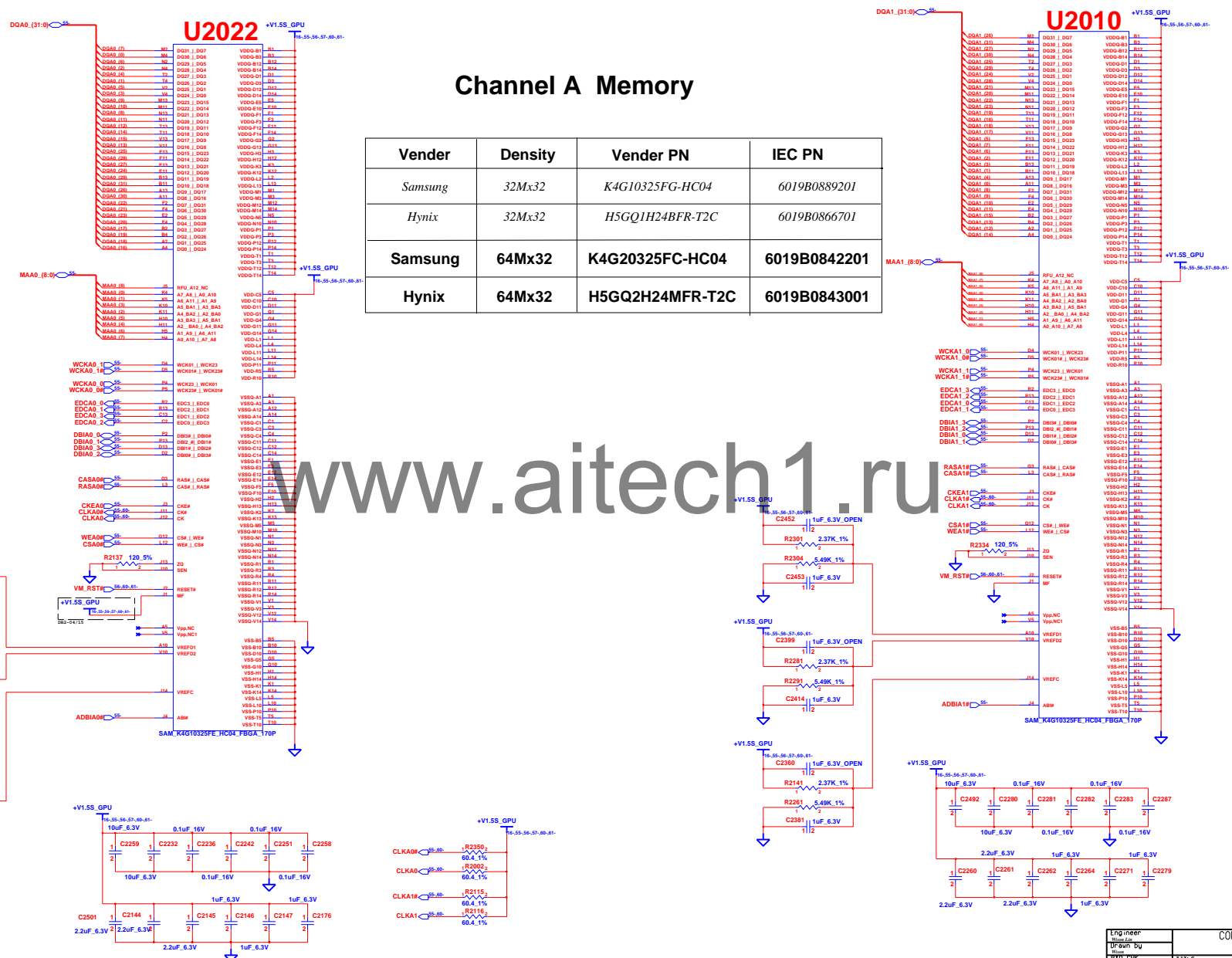
INVENTEC

TITLE LOUIS GPU-4			
SIZE A3	CODE CS	DOC. NUMBER 1310A24686	REV A02
CHANGE by Wilson		31-Aug-2011	
SHEET 56		OF 64	









Vender	Density	Vender PN	IEC PN
<i>Samsung</i>	<i>32Mx32</i>	<i>K4G10325FG-HC04</i>	<i>6019B0889201</i>
<i>Hynix</i>	<i>32Mx32</i>	<i>H5GQ1H24BFR-T2C</i>	<i>6019B0866701</i>
Samsung	64Mx32	K4G20325FC-HC04	6019B0842201
Hynix	64Mx32	H5GQ2H24MFR-T2C	6019B0843001

www.aitech1.ru

Engineer	COMPANY NAME	
When Lit	Address	Size sq ft
Drawn by	City	
When	TITLE	
R&D CHK	LOUIS	
DWG CTRL CHK		
MFG ENGR CHK		

Channel B Memory

www.aitech1.ru

U2056

+V1.55_GPU

MF = 1 For MIRROR

Engineer	COMPANY NAME
Desig. BU	City
Rev. CHK	DATE
Doc. Ctrl. CHK	
Proj. Eng. CHK	
Checked by	Date
Designed by	Date
Rev. 1	Rev. 2
Rev. 3	Rev. 4
Rev. 5	Rev. 6
Rev. 7	Rev. 8
Rev. 9	Rev. 10
Rev. 11	Rev. 12
Rev. 13	Rev. 14
Rev. 15	Rev. 16
Rev. 17	Rev. 18
Rev. 19	Rev. 20
Rev. 21	Rev. 22
Rev. 23	Rev. 24
Rev. 25	Rev. 26
Rev. 27	Rev. 28
Rev. 29	Rev. 30
Rev. 31	Rev. 32
Rev. 33	Rev. 34
Rev. 35	Rev. 36
Rev. 37	Rev. 38
Rev. 39	Rev. 40
Rev. 41	Rev. 42
Rev. 43	Rev. 44
Rev. 45	Rev. 46
Rev. 47	Rev. 48
Rev. 49	Rev. 50
Rev. 51	Rev. 52
Rev. 53	Rev. 54
Rev. 55	Rev. 56
Rev. 57	Rev. 58
Rev. 59	Rev. 60
Rev. 61	Rev. 62
Rev. 63	Rev. 64
Rev. 65	Rev. 66
Rev. 67	Rev. 68
Rev. 69	Rev. 70
Rev. 71	Rev. 72
Rev. 73	Rev. 74
Rev. 75	Rev. 76
Rev. 77	Rev. 78
Rev. 79	Rev. 80
Rev. 81	Rev. 82
Rev. 83	Rev. 84
Rev. 85	Rev. 86
Rev. 87	Rev. 88
Rev. 89	Rev. 90
Rev. 91	Rev. 92
Rev. 93	Rev. 94
Rev. 95	Rev. 96
Rev. 97	Rev. 98
Rev. 99	Rev. 100

[illegible]

A

B

C

D

E

F

G

H

BLANK

www.aitech1.ru

Engineer Drawn by	COMPANY NAME	
R&D CHK	Title Address City	Sizes
DWG ENGR CHK		
MFG ENDR CHK		
Checked by Date Changed Title Changer DA CHK RLY Drawing Number Sheet 62 of 64		

1 2 3 4 5 6 7 8 9 10 11 12

BLANK

www.aitech1.ru

Engineer Drawn by	COMPANY NAME	
DWG CHK	Title	City
DWG ENDR CHK	Address	
Checked by Date Changed	Date Changed	Time Changed
DA CHK	REV	Drawing Number Sheet 62 of 64

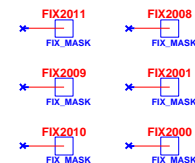
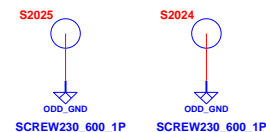
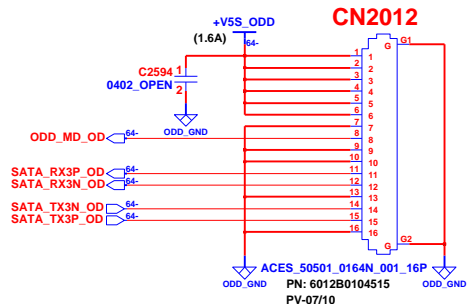
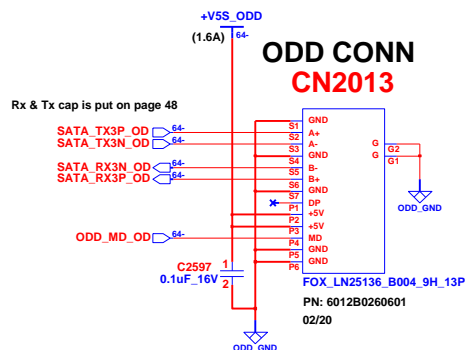
[illegible]

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www.aitech1.ru

Engineer	COMPANY NAME	
Drawn by	Address	
Rev	City	State
QA CHK	TITLE	
DWG CTRL CHK	LOOSE	
ENG ENGR CHK	REV	

ODD Board: 6050A2459601



www.aitech1.ru

INVENTEC

TITLE: **LOUIS**
ODD Board

SIZE A3	CODE CS	DOC. NUMBER 1510A24596	REV A02
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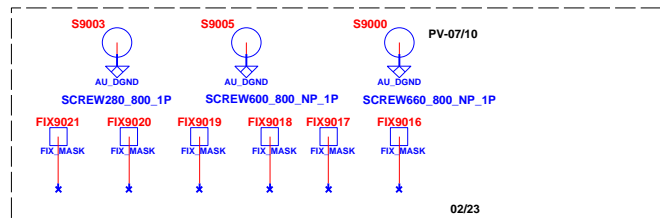
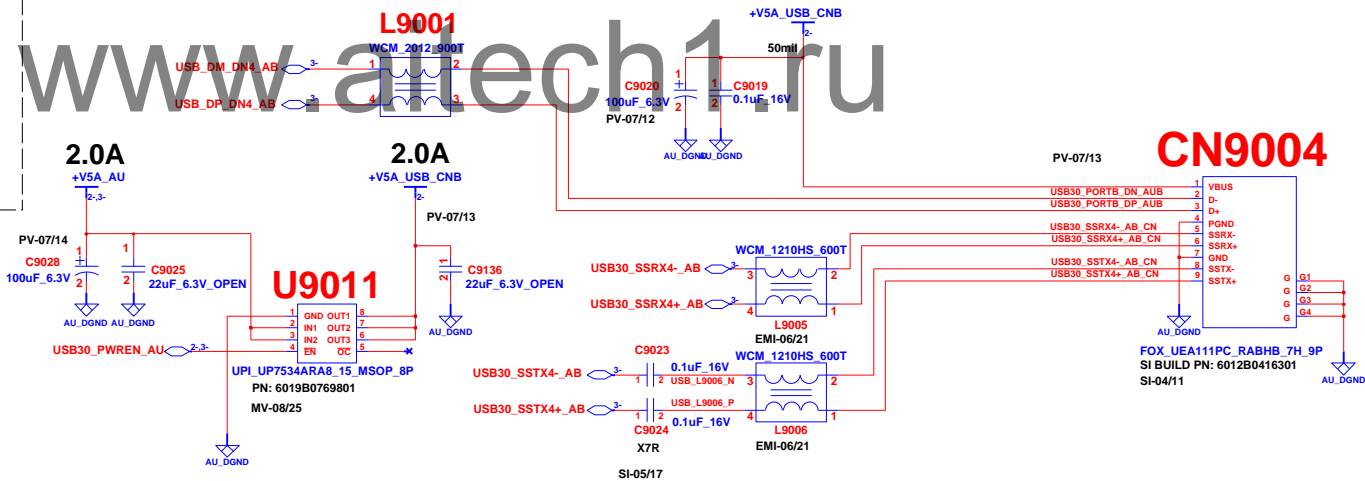
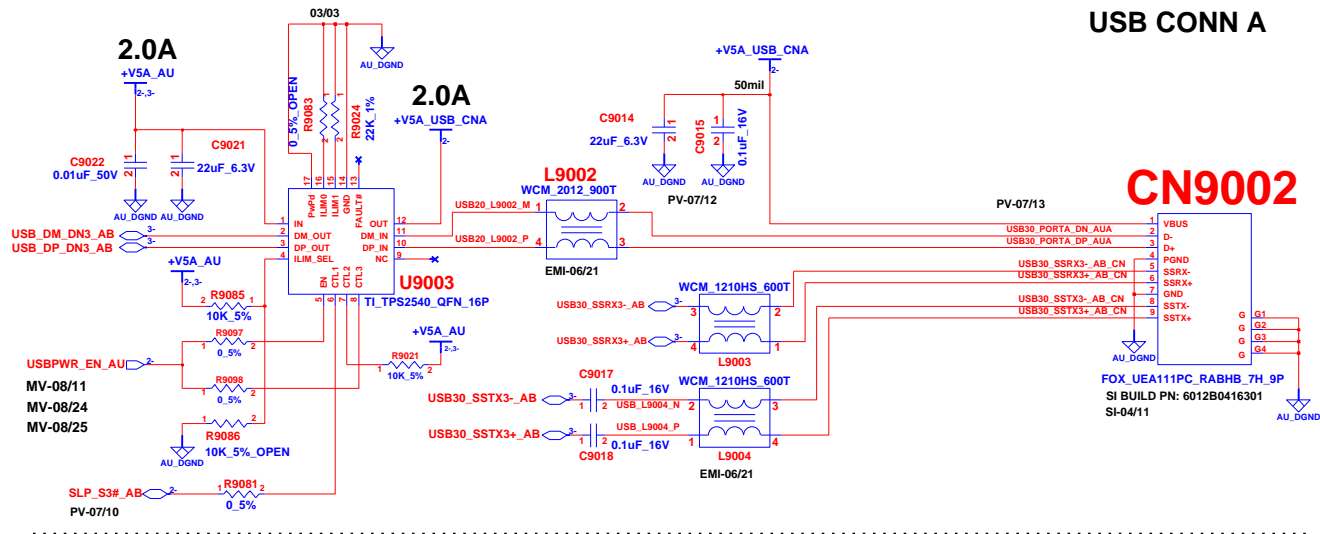
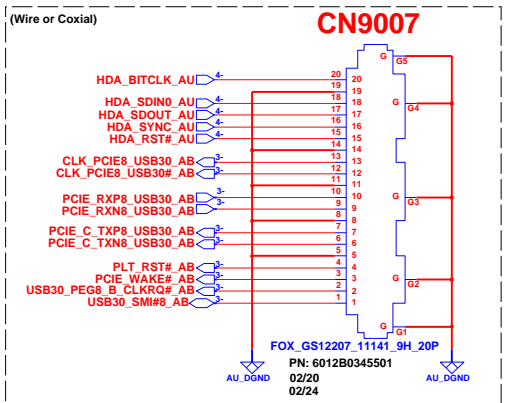
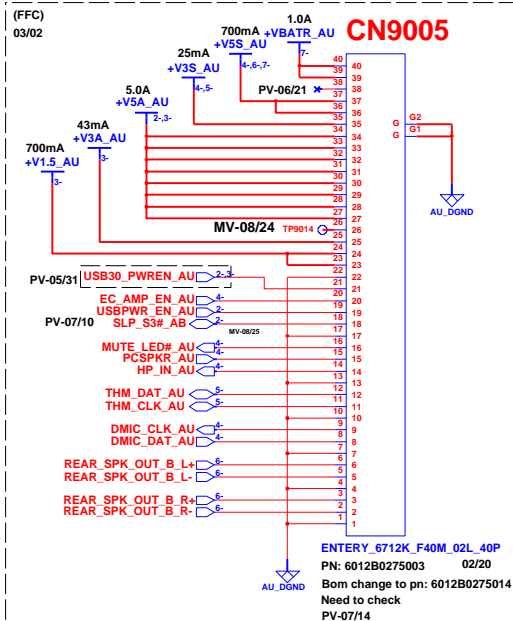
CHANGE by: **Wilson** 31-Aug-2011

SHEET 64 OF 64

	1	2	3	4	5	6	7	8																																																																															
A	THIS DRAWING AND SPECIFICATIONS, HEREIN, ARE THE PROPERTY OF INVENTEC CORPORATION AND SHALL NOT BE REPRODUCED, COPIED, OR USED IN WHOLE OR IN PART AS THE BASIS FOR THE MANUFACTURE OR SALE OF ITEMS WITHOUT WRITTEN PERMISSION, INVENTEC CORPORATION 2010 ALL RIGHT RESERVED								A																																																																														
	HSF Property : Halogen-Free																																																																																						
B									B																																																																														
C									C																																																																														
D	<div><h1>Daughter Board</h1><div><div>MV Build</div><div>2011/09/01</div></div></div>								D																																																																														
E	<p>PAGE1 TO PAGE7 USB BOARD (AUDIO BOARD) PN: 6050A2459401 ; 1310A2459401 PAGE8 LDPS BOARD PN: 6050A2459701 PAGE9 CARD READER BOARD PN: 6050A2458801 PAGE10 POWER BOARD PN: 6050A2459801 PAGE11 3D IR BOARD PN: 6050A2459901</p>								E																																																																														
F	<table><tr><td></td><td></td><td></td></tr><tr><td></td><td></td><td></td></tr><tr><td></td><td></td><td></td></tr><tr><td></td><td></td><td></td></tr><tr><td></td><td></td><td></td></tr><tr><td></td><td></td><td></td></tr><tr><td></td><td></td><td></td></tr><tr><td></td><td></td><td></td></tr><tr><td></td><td></td><td></td></tr><tr><td>DATE</td><td>CHANGE NO.</td><td>REV</td></tr></table>																												DATE	CHANGE NO.	REV	<table><tr><td>DRAWER</td><td>EE</td><td>DATE</td><td>POWER</td><td>DATE</td><td rowspan="4">INVENTEC</td></tr><tr><td>DESIGN</td><td></td><td></td><td></td><td></td></tr><tr><td>CHECK</td><td></td><td></td><td></td><td></td></tr><tr><td>RESPONSIBLE</td><td></td><td></td><td></td><td></td></tr><tr><td colspan="5">SIZE = 3</td><td>VER :</td><td rowspan="2">LOUIS</td></tr><tr><td colspan="5">FILE NAME : XXXX-XXXXXX-XX</td></tr><tr><td>PN</td><td colspan="4">XXXXXXXXXXXX</td><td>SIZE CODE</td><td>DOC. NUMBER</td><td>REV</td></tr><tr><td></td><td colspan="4"></td><td>A3 CS</td><td>1 OF 11</td><td>A02</td></tr></table>						DRAWER	EE	DATE	POWER	DATE	INVENTEC	DESIGN					CHECK					RESPONSIBLE					SIZE = 3					VER :	LOUIS	FILE NAME : XXXX-XXXXXX-XX					PN	XXXXXXXXXXXX				SIZE CODE	DOC. NUMBER	REV						A3 CS	1 OF 11	A02	F
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AU Board-1

AU Board CN on DB



INVENTEC			
TITLE LOUIS AUDIO & USB Board			
SIZE A3	CODE CS	DOC. NUMBER 1310A24594	REV. A0

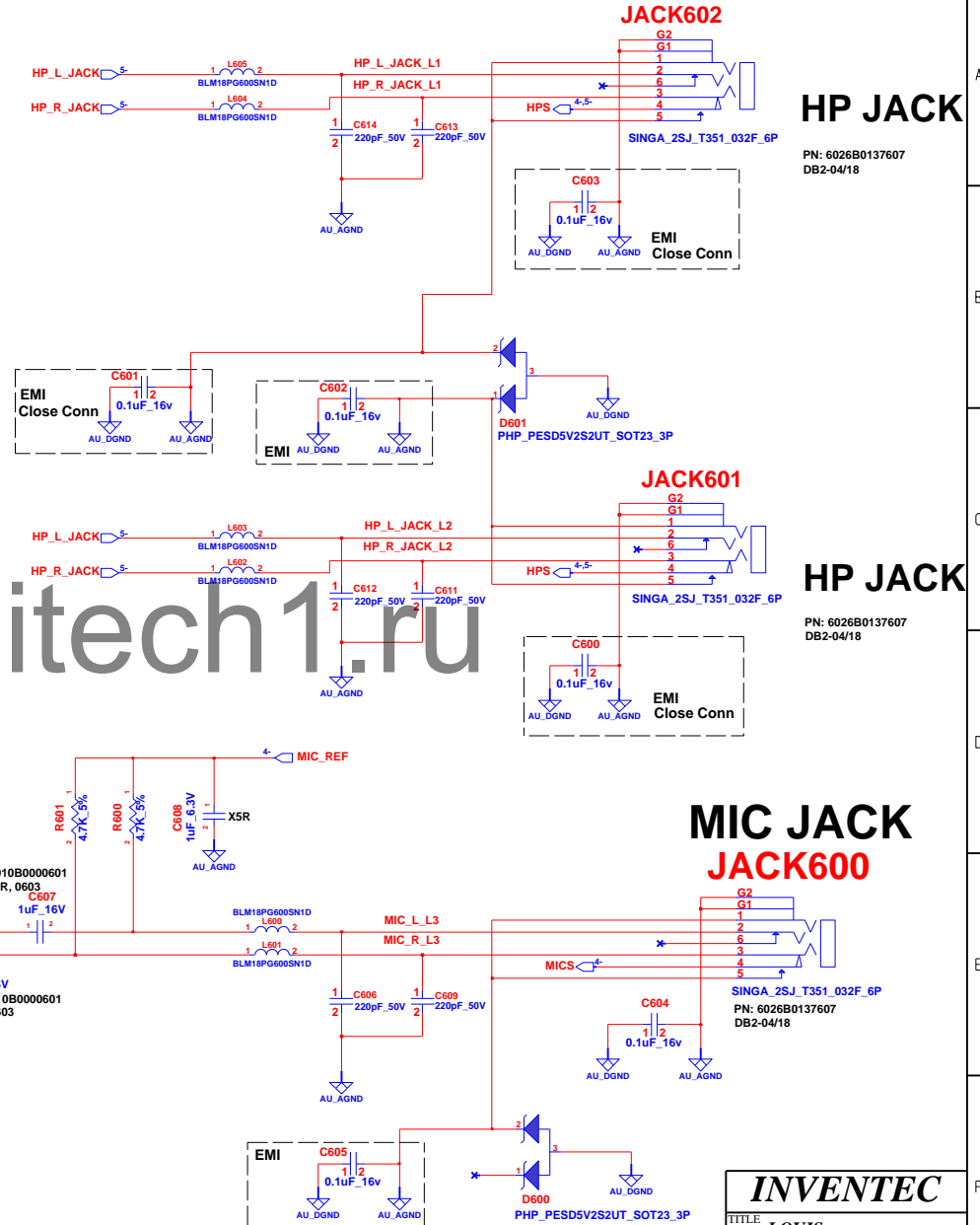
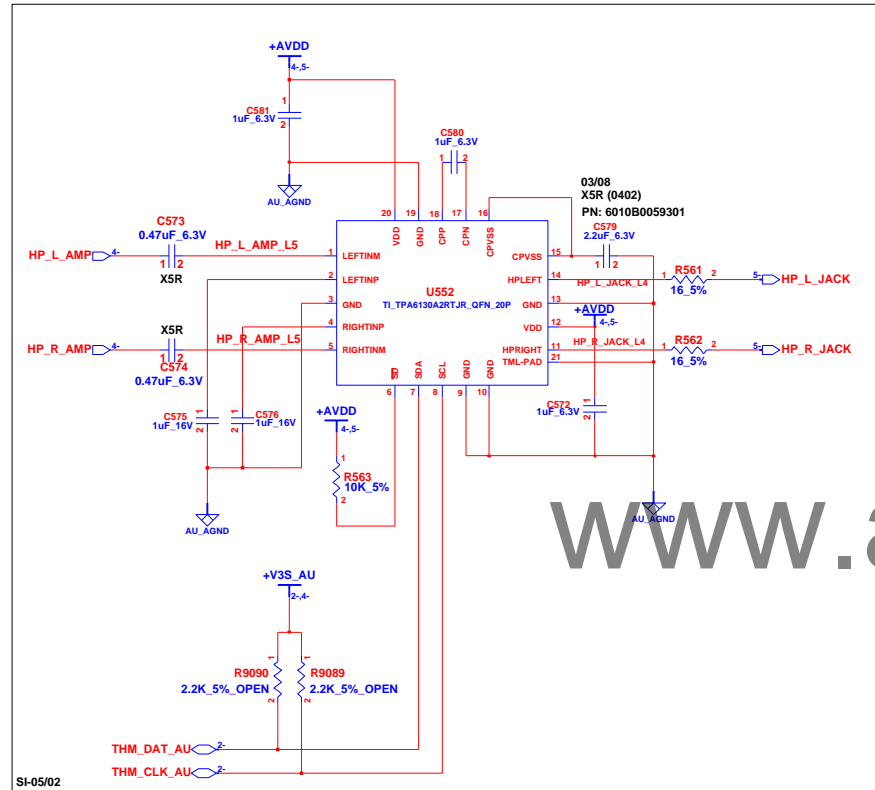
A



1

CHANGE by	Wison	31-Aug-2011
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AU Board-3

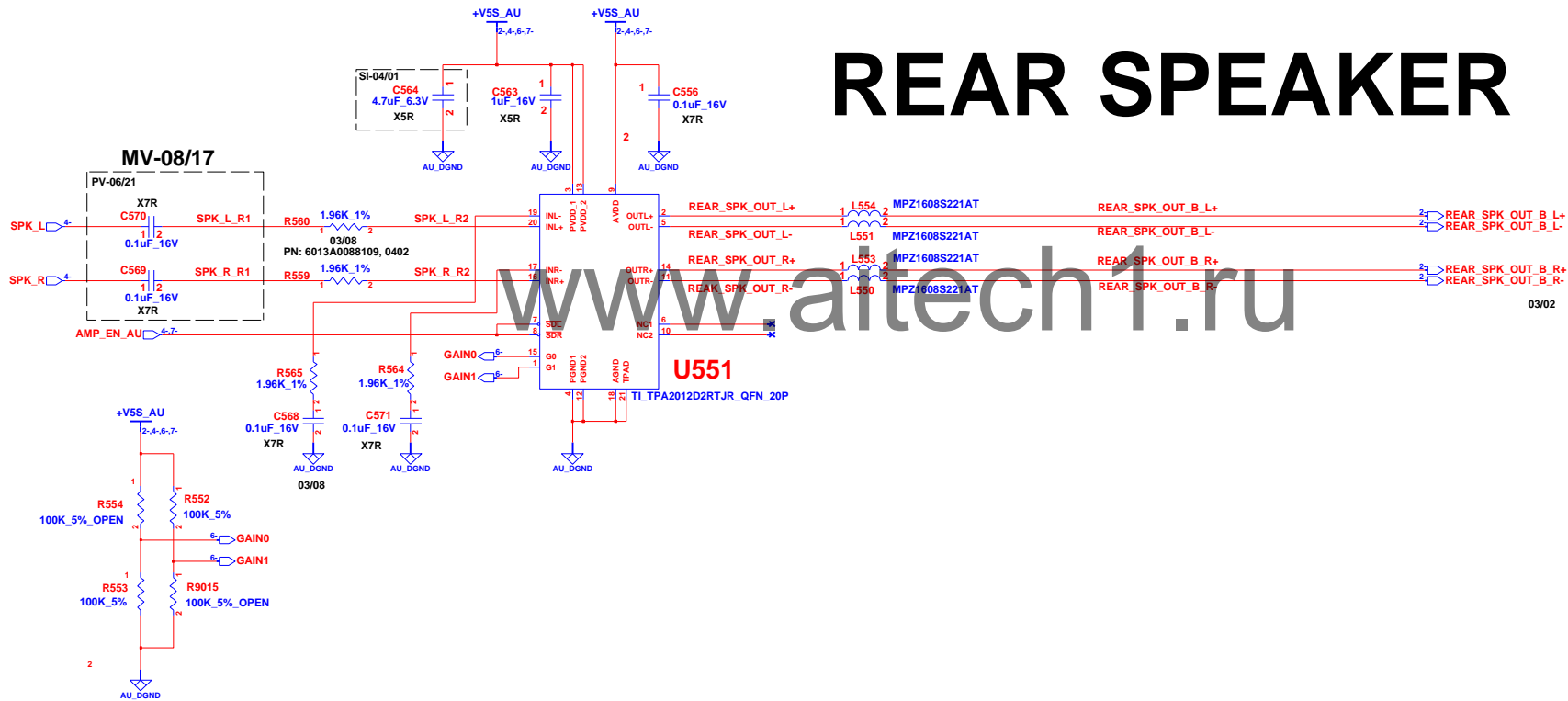


INVENTEC

TITLE LOUIS AUDIO & USB Board			
SIZE A3	CODE CS	DOC. NUMBER 1310A24594	REV A02
CHANGE by Wilson		31-Aug-2011	
SHEET 5		OF 11	

AU Board-4

REAR SPEAKER



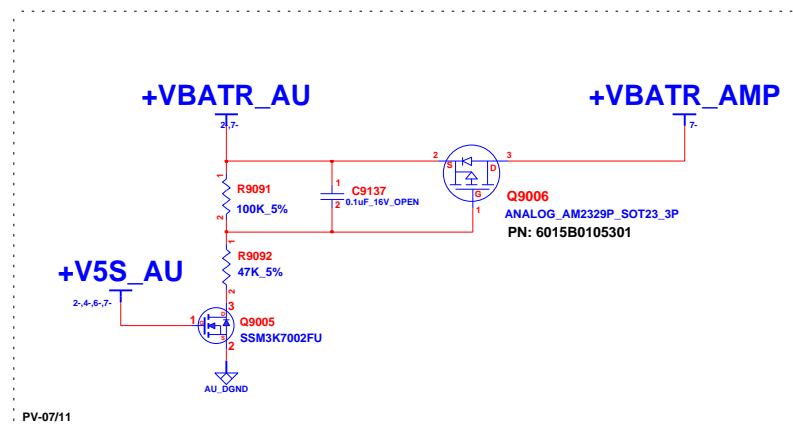
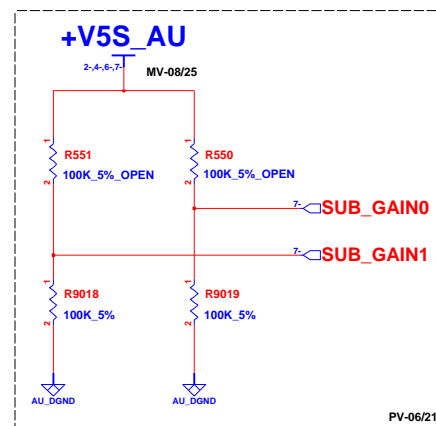
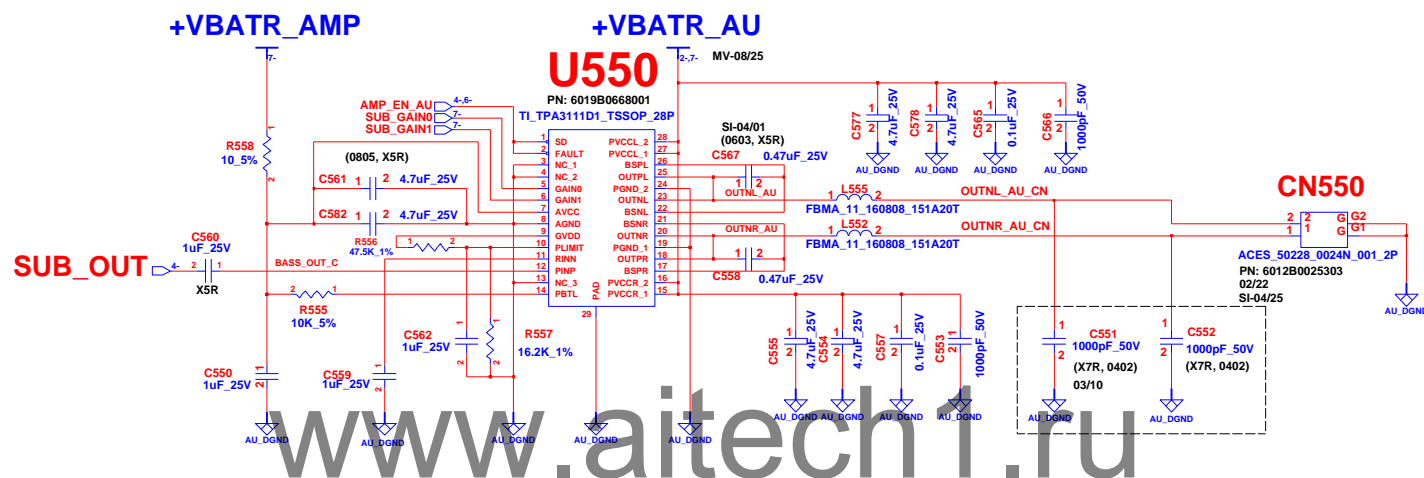
INVENTEC

TITLE LOUIS ODD EXT BOARD			
SIZE A3	CODE CS	DOC. NUMBER 1310A24594	REV A02

CHANGE by **Wison** 31-Aug-2011

SHEET 6 OF 11

AU Board-5 SUBWOOFER



INVENTEC

TITLE: **LOUIS**
Power SW Board
SIZE: **A3** CODE: **CS** DOC. NUMBER: **1310A24594** REV: **A02**

CHANGE by: **Wison** 31-Aug-2011

SHEET 7 OF 11

PCB 6050A2459701 (3D) 1310A2459701
(2D) 1310A2459702

[illegible]

03/08
PN: 6011B0143701

D9004 (IR11)

D9002

EV95-21C/TR7
PN: 6011B0142701

3D-IR_LED#_LB

+V5A_LB

R9001
40Ω ±1%
(0402)

R9002
40Ω ±1%
(0402)

EV1-IR11-21C-2P

EV1-IR95-21C-TR7-2P

PV-07/10

TITLE			
LOUIS WIRELESS Board			
SIZE	CODE	DOC. NUMBER	REV
A3	CS	1310A24597	A02

CHANGE by	<i>Wison</i>	<i>31-Aug-2011</i>
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SHEET 8 OF 11

Card Reader BOARD

Card Reader BOARD

U9008 (PN: 6019B0789201)

Card Reader CONN

Wireless Audio

SATA LED & HDD-HALTED LED

POWER LED

INVENTEC

LOUIS WIRELESS Board

CHANGE by Wilson **31-Aug-2011**

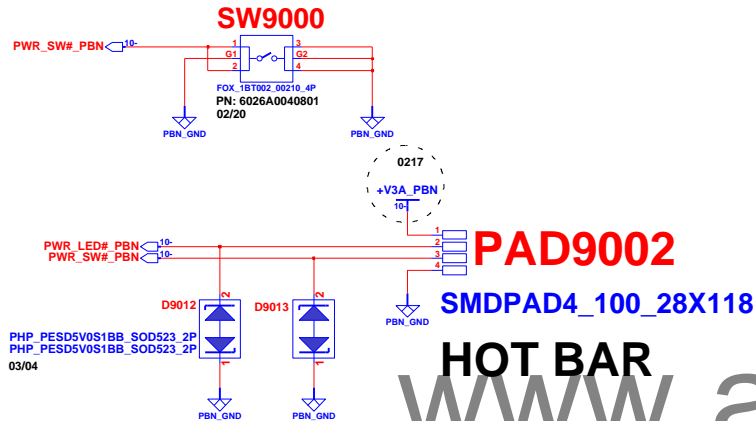
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8

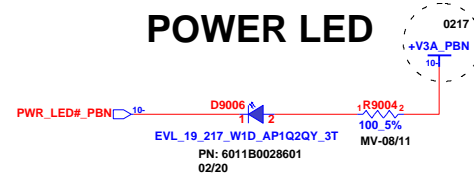
CHANGE by <i>Wison</i>		31-Aug-2011	SHEET 9 OF 11	
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Power Button

POWER BUTTON



POWER LED



HOT BAR

HOT BAR
www.aitech1.ru

INVENTEC

TITLE	<i>LOUIS</i>
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SIZE A3	CODE CS	DOC. NUMBER 1310A24598	REV A02
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CHANGE by	Wison	31-Aug-2011
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CS	1310A24598
SHEET	10 OF 11

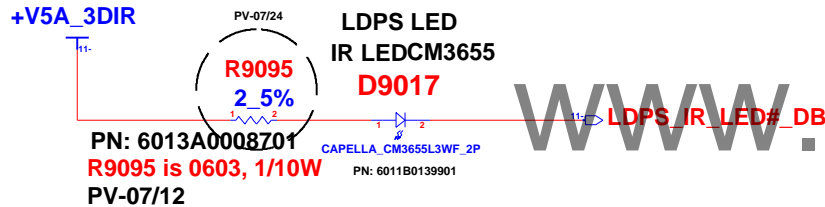
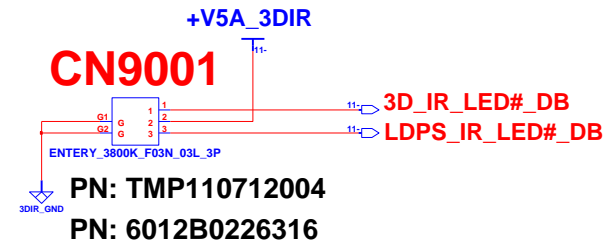
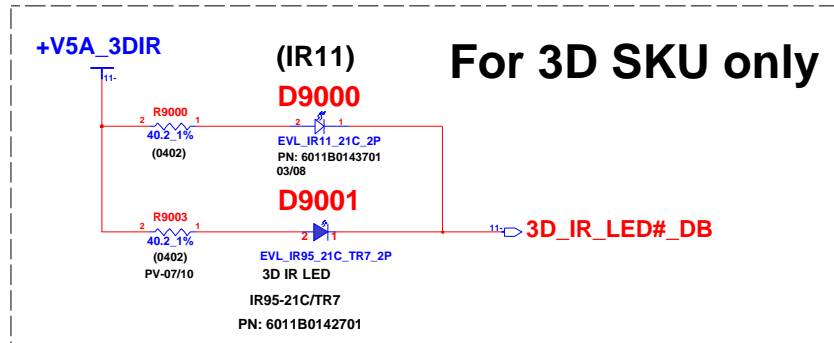
3D_IR_BOARD

PCB: 6050A2459901

2D: 1310A2459902

3D: 1310A2459901

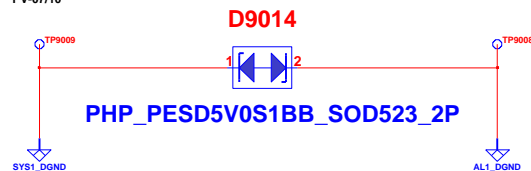
FIX9000
FIX_MASK
★



PV-07/14
Need to check

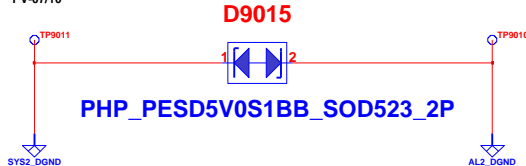
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PV-07/10



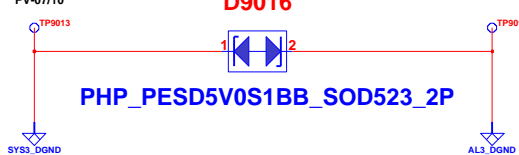
ESD Board 2: 6050A2483301

PV-07/10



ESD Board 3: 6050A2483401

PV-07/10



INVENTEC

TITLE
LOUIS

SIZE CODE DOC NUMBER REV
A3 CS 1310A24599 A02

CHANGE by **Wilson** 31-Aug-2011

SHEET 11 OF 11